**Abstract:**

The aim of the experiment was to practice doing different digital components by using Quartus program by building a Verilog codes and block diagrams. In addition, to learn how to deal with the digital components that make up a system and to become more familiar with the **FPGA** programming, by building a simple security system using Quartus program, and then using that FPGA board to download the system we built on it.

**Table of Content:**

**[Content] ……………………………………………… [Page]**

* Introduction …………………………………………………… [1]
* Theory …………………………………[1]
* 2x4 Decoder …………………………...[2]
* 8x3 priority encoder …………………...[3]
* 7-segement driver………………………[4]
* Memory system ………………………..[5]
* Comparator …………………………….[6]
* 4-input AND gate ……………………...[6]
* Procedure ………………………………………………………[6]
* Conclusion …………………………………………………......[10]
* References ……………………………………………………..[10]