

Solution



BIRZEIT UNIVERSITY  
Electrical and Computer Engineering Department  
Digital Systems – ENCS234  
Midterm Exam  
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Question 1

A. 5 points] Convert  $(154.125)_8$  to Base-16 format

$$(154.125)_8 \rightarrow (1101100.0010101)_2 = (6C.2A8)_{16}$$

B. 9 points] Perform the following subtraction using signed 2's complement 8 bit representation  $(-74 - 66)$  and determine the overflow?

$$\begin{array}{r}
 \boxed{10} \quad \overset{\overset{\cdot}{\cdot}}{\overset{\overset{\cdot}{\cdot}}{\cdot}} \\
 10110110 \\
 - 10111110 \\
 \hline
 \boxed{11} 01110100 \\
 \text{Overflow}
 \end{array}$$

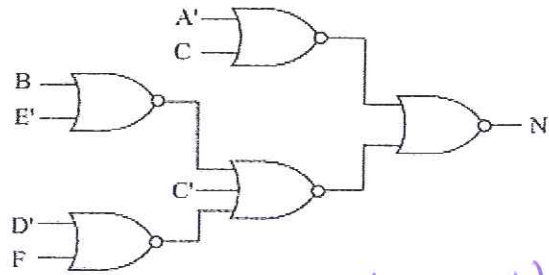
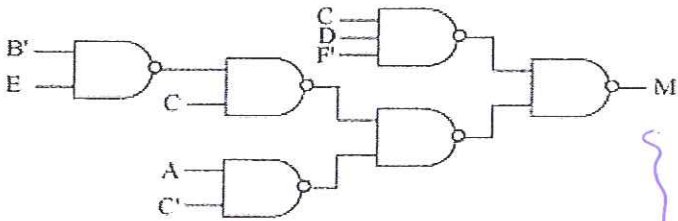
$$\begin{array}{l}
 74 \rightarrow 01001010 \\
 74 \xrightarrow{2's \text{ comp}} 10110110 \\
 66 \rightarrow 01000010 \\
 66 \xrightarrow{2's \text{ comp}} 10111110
 \end{array}$$

C. 6 points] Perform the following addition using BCD representation  $(874 + 236)$

0010	0011	0110
1000	0111	0100
-----	-----	-----
1010	1010	1010
110	110	110
-----	-----	-----
0001	0001	0000
-----	-----	-----
1	1	0

D. 10 points] A certain ENCS234 student claims (أدعى) that the two circuits below implement the same Combinational logic function. Prove or disprove his claim. Show your derivation.

Prove



$$(A'+C)(B'E+C') + CDF'$$

$$A'B'E + A'C' + B'CE + CDF'$$

$$A'C' + B'CE + CDF'$$

more explanation

$$B'E + A'C' + B'CE + CDF'$$

$$B'E(c+c') + A'C' + B'CE + CDF'$$

$$B'E(A'+1) + A'C'(B'E+1) + CDF'$$

$$= B'CE + A'C' + CDF'$$

$$(A'+C)(B'E+C'+DF')$$

$$= A'B'E + A'C' + A'DF' + B'CE + CDF'$$

$$= A'B'E + A'C' + B'CE + CDF'$$

$$= A'C' + B'CE + CDF'$$

More explanation

$$A'B'E + A'C' + A'DF' + B'CE + CDF'$$

$$= A'B'E(c+c') + A'C' + A'DF'(c+c') + B'CE + CDF'$$

$$= B'CE + CDF'$$

$$= B'CE + A'C' + CDF'$$

Same function

or you need to build the truth table

**Question 2**

A. 22 points] Simplify using QM Tabulation method the following function  
 $F(A,B,C,D,E) = \sum(0, 1, 2, 9, 11, 12, 13, 27, 28, 29)$

List 1

minterm	ABCDE
✓ 0	00000
✓ 1	00001
✓ 2	00010
<hr/>	
✓ 9	01001
✓ 12	01100
<hr/>	
✓ 11	01011
✓ 13	01101
<hr/>	
✓ 28	11100
<hr/>	
✓ 27	11101
✓ 29	11101

List 2

minterm	ABCDE	
011	0000	PI2
012	000	PI3
<hr/>		
119	0-001	PI4
<hr/>		
9111	010	PI6
9113	01-01	PI5
12113	0110	✓
12128	-1100	✓
<hr/>		
11127	-1011	PI7
13129	-1101	
28129	1110-	

List 3

minterm	ABCDE	PI
1213128129	-110-	PI1

	0	1	2	9	11	12	13	27	28	29
PI1*										
PI2	x	x								
PI3*	x		x							
PI4		x		x			x			
PI5				x						
PI6				x	x				x	
PI7*						x				

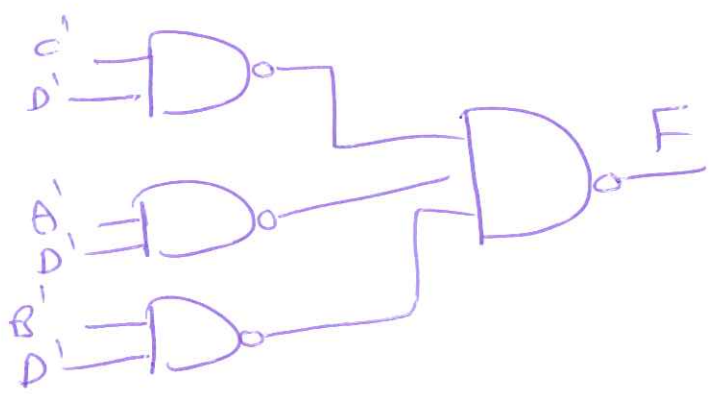
PI2	1	9
PI4	x	x
PI5		x
PI6		x

$$f(A,B,C,D,E) = \text{PI1} + \text{PI3} + \text{PI4} + \text{PI7} + BCD' + A'BC'E' + ACD'E + BCDE$$

B. 10 points] Implement the following function using together with the don't care using NAND-NAND  
 $F(A,B,C,D) = \prod(1, 3, 5, 7, 11, 14, 15)$   
 $D(A,B,C,D) = \prod(0, 8, 9, 13)$

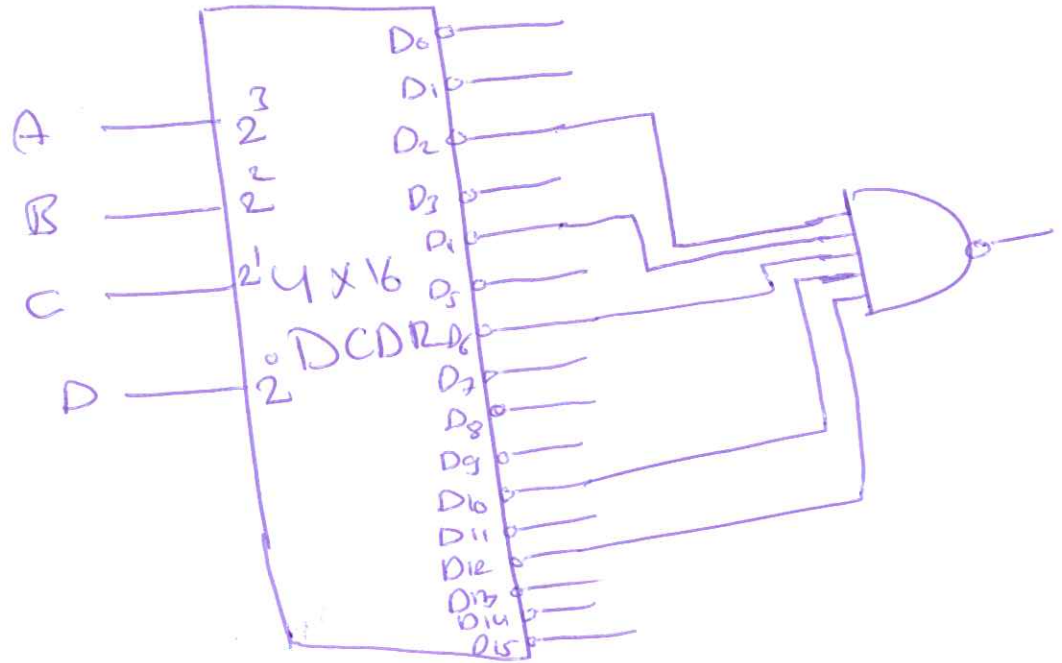
$$F = C'D' + A'D' + B'D'$$

AB \ CD	00	01	11	10
00	(X)	0	0	(1)
01	(1)	0	0	(1)
11	(1)	X	0	0
10	(X)	X	0	(1)



C. 8 points] Implement the same function in part B using active low 4-to-16 decoder

OR  
You can  
use AND  
gate  
with  
TT



**Question 3**

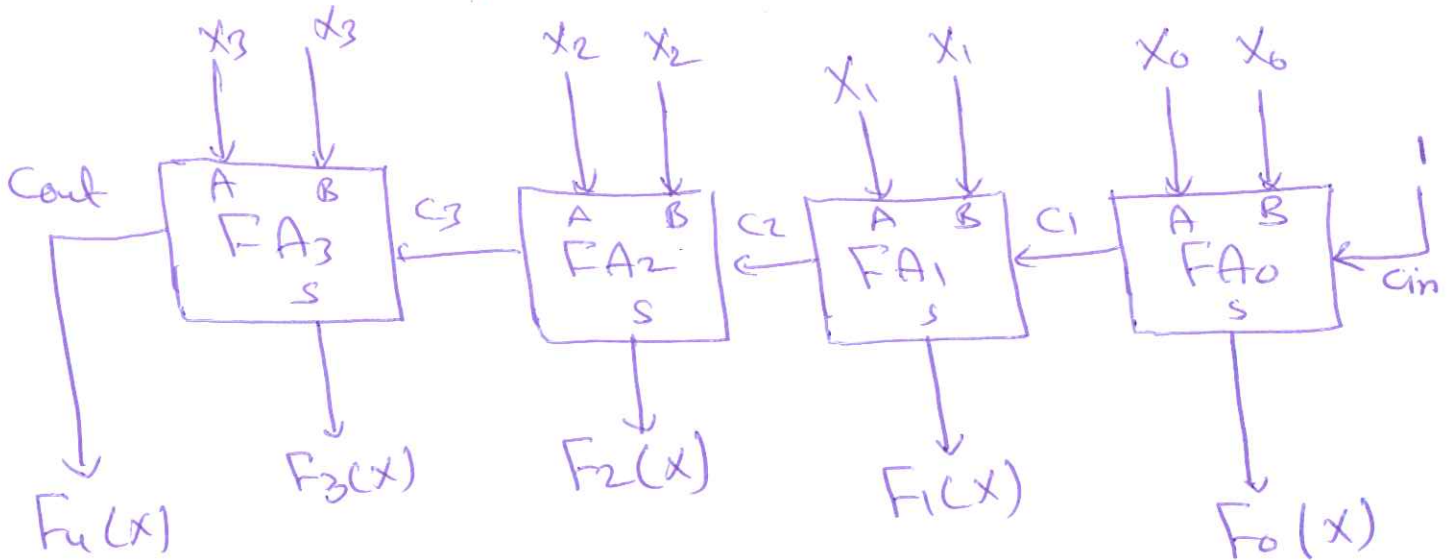
A. 10 points] Design a digital circuit to realize the following function

$$F(X) = 2 \cdot X + 1$$

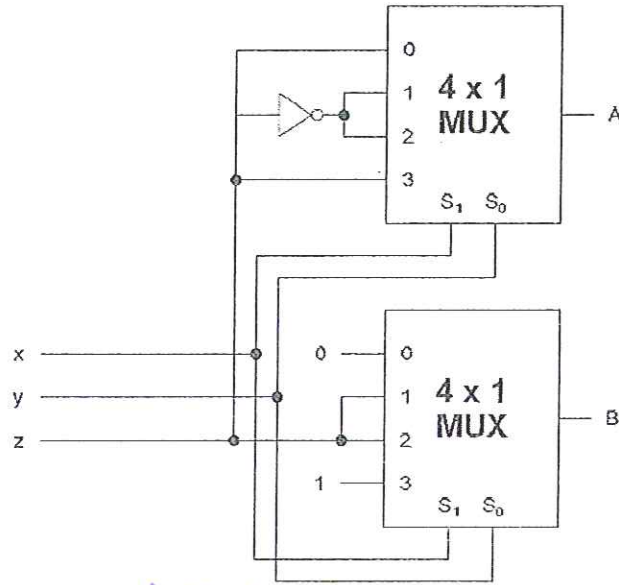
$$2X + 1 = \begin{matrix} + X_4 X_3 X_2 X_0 \\ + X_4 X_3 X_2 X_0 \\ \hline \end{matrix}$$

Where X is a 4-bit unsigned binary number (e.g,  $X = X_3 X_2 X_1 X_0$ )

Hint: Use adders as block diagram



B. 10 points] Determine the outputs functions  $A$  and  $B$  as sums of minterms.



input			output	
$x$	$y$	$z$	$A$	$B$
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

$$A = \sum (1, 2, 4, 7)$$

$$B = \sum (3, 5, 6, 7)$$

Question 4

10 points] Show a truth table of a 4-input combinational circuit whose output is equal to 1 if the input variables have more 1's than 0's or number of 1's equal number of 0's.

A	B	C	D	F
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1