

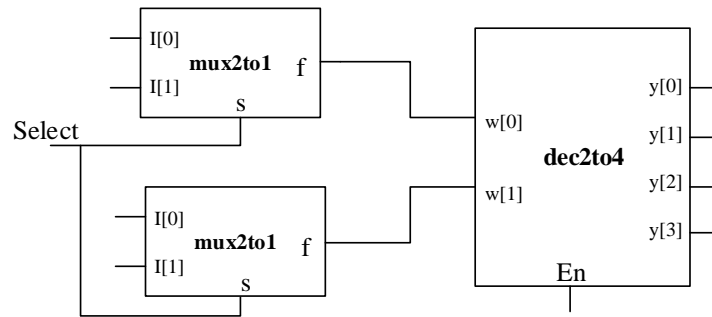


Faculty of Engineering & Technology – Electrical & Computer Engineering Department  
Digital Systems ENCS234

HDL Homework

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**Q1:** Given the following Combinational circuit, Use Verilog HDL on Quartus tool to



1. Write a Verilog HDL code to describe the module `mux2x1` // **this module name must be your last name**
2. Write a Verilog HDL code to describe the module `DCDR2x4` // **this module name must be your first name**
3. Write a Verilog HDL code to describe the whole system **structurally** from its subsystems // **this module name must be your university number**

**Q2:** Given the following Combinational circuit, Use Verilog HDL on Quartus tool to

1. Write a Verilog HDL code to describe the module `DCDR2x4` // **this module name must be your last name**
2. Write a Verilog HDL code to describe the whole system **structurally** from its subsystems // **this module name must be your university number**

