

## **ENCS2380-** Computer Organization and Microprocessor

## Dr. Ayman Hroub

Quiz #1

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Student Name:	••••••	 ••••••	••••

Student ID: .....

Quiz Time: 15 minutes

**Question #1 (5 Points):** Choose the correct answer (multiple choice): 2. 1. B A 3. D 4. Α 5. B 1. One of the following is not an architectural attribute of the processor B. Memory A. Addressing Modes C. Instructions' D. I/O Mechanisms **Technology** Formats 2. The smallest addressable unit of data in memory is: A. Byte B. Bit C. Word D. Half Word 3. The instruction execution stage in which the instruction is interpreted D. Decode A. Execute B. Memory Access C. Fetch The addressing mode in which the operand's address is one of the instruction's fields: 4. A. Direct B. Indirect C. Register Indirect D. Immediate 5. In the processor design, the organization is decided before the processor's architecture is fixed A. True B. False

**Question #2 (5 Points):** A program is run on a 1 MHz processor. The following table shows the instruction mix of this program, and the CPI of each instruction type. Answer the questions below and show your solution steps clearly.

Instruction Type	Instruction Count	Cycles per Instruction
Arithmetic and Logic	4000	1
Data Transfer	3000	2
Control Instructions	1000	2
Floating Point	2000	2

- a. Calculate the effective CPI of this program on this processor (**3 Points**)
- b. Calculate the execution time of this program on this processor (2 Points)

## **Good Luck!**

Good Luck!  $CPI = \sum_{i=1}^{n} (CPI_i \times Instruction Count i)$  Total instruction counttotal instruction count = 4000 + 3000 + 1000 + 2000 = 10000 CPI = 1 × 0.4 + 2× 0.3 + 2× 0.1 + 2× 0.2 = 0.4 + 0.6 + 0.2 + 0.4 = [1.6] CPU time: Instruction count \* CPI \* clock  $= 10000 \times 1.6 \times 1 = 0.0165$ 16 ms