

Faculty of Engineering and Technology

Electrical and Computer Engineering Department

Advanced Digital Design ENCS533

Homework#1

Given the following figure

1. Write a VHDL description for the 3x8 decoder.
2. Write a VHDL description for the 4x1 MUX.
3. Writa a Structural VHDL Description for the Circuit.
4. Derive the truth table and the equation for F, and write a behavioral VHDL description for F.

