

Design Procedure

Example: Design a gated latch circuit with two inputs G (gate) and D (Data), and one output Q . The value of D is transferred to Q when $G=1$. When $G=0$ the value of Q will not change.

Solution

Step 1: get the primitive flow table (flow table with only one stable total state in each row), and the total state consists of the internal state combined with inputs \Rightarrow in this example we may have up to 8 ~~total~~ stable total states

D	G	Q	Stable??
0	0	0	yes
0	0	1	yes
0	1	0	yes
0	1	1	no
1	0	0	yes
1	0	1	yes
1	1	0	no
1	1	1	yes

\Rightarrow in this example we have 6 stable total states, let us now rearrange them

state	Inputs		output Q	Comments
	D	G		
a	0	1	0	$\phi = 0$ because $G=1$
b	1	1	1	$\phi = 0$ because $G=1$
c	0	0	0	after state a or d
d	1	0	0	after state c
e	1	0	1	after state b or f
f	0	0	1	after state e

⇒ now we can draw the flow table which has one row for each state and one column for each input combination ⇒ the flow table for this example will have 6 rows & 4 columns

	DG			
	00	01	11	10
a	c, -	(a), 0	b, -	- , -
b	- , -	a, -	(b), 1	e, -
c	(c), 0	a, -	- , -	d, -
d	c, -	- , -	b, -	(d), 0
e	f, -	- , -	b, -	(e), 1
f	(f), 1	a, -	- , -	e, -

a. fill in one square in each row belongin to the stable state in that row

b. because both inputs are not allowed to change simultaneously we can enter dash marks (don't care) in each row that differs in two or more variable from the input variable associated with stable states.

e. to fill the empty ~~rows~~ squares, we will use the comments in the previous table to see how we can reach a stable total state

Step 2: use the implication table to merge the flow table by finding the maximal compatibles of the total states

- any two states may be
 - a. incompatible
 - b. compatible
 - c. equivalent

any equivalent ~~set~~ pair of states are compatible.
any incompatible pair of states are inequivalent.
two compatible states are not necessarily to be equivalent.

~~Example of using Implication table to find equivalent states.~~ Two states are said to be equivalent if they have the same output and go to the same or equivalent next states

Ex:

Present state	next state		output	
	$x=0$	$x=1$	$x=0$	$x=1$
a	c	b	0	1
b	d	a	0	1
c	a	d	1	0
d	b	d	1	0

b	c,d		
c	x	x	
d	x	x	a,b
	a	b	c

this means that (a,b) are equivalent imply (c,d)
and (c,d) imply (a,b)

\Rightarrow a equivalent to b and
c equivalent to d

(a,b), (c,d)
↓ ↓
a c

Ex.

Present state	next state		output	
	x=0	x=1	x=0	x=1
a	d	a	0	0
b	e	a	0	0
c	g	f	0	1
d	a	d	1	0
e	a	d	1	0
f	c	b	0	0
g	a	e	1	0

b	d,e ✓					
e	x	x				
d	x	x	x			
e	x	x	x	✓		
f	c,d x	c,e a,b x	x	x	x	
g	x	x	x	d,e ✓	d,e ✓	x
	a	b	c	d	e	f

$(a, b), (d, e), (d, g), (e, g)$

$\Rightarrow (a, b), (c), (d, e, g), (f)$

\downarrow
 $(a), (c), (d), (f)$

* Compatible states

Two states are said to be compatible if for each possible input they have the same output whenever specified and their next state are compatible whenever they are specified. All don't care conditions marked with dashes have no effect when searching for compatible states because they represent unspecified conditions.

b	✓				
c	✓	d, ex			
d	✓	d, ex	✓		
e	c, fx	✓	c, fx d, ex	✗	
f	c, fx	✓	✗	✗	✓
	a	b	c	d	e

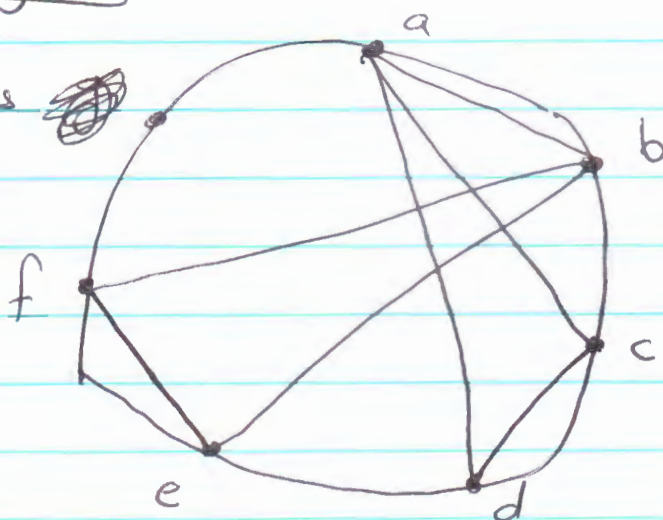
\Rightarrow the compatible pairs are

$(a, b), (a, c), (a, d), (b, e), (b, f), (c, d), (c, f)$

- Having found all compatible pairs, the next step is to find larger sets of states that are compatible. The maximal compatible is a group of compatibles that contains all the possible combinations of compatible states.

using merger-diagram

⇒ maximal compatibles ~~are~~
(a, c, d), (b, e, f),
(a, b).



- after finding the maximal compatibles, we must find a minimal collection of compatibles that covers all the states and is closed.

The set will cover all the states if it includes all the states of the original table.

The closure condition is satisfied if there are no implied states or if the implied states are included within the set.

⇒ in our example, minimal collection of compatibles are

(a, c, d) and (b, e, f)

this collection includes all states, and it is closed because there is no implied states.

(a, c, d) (b, e, f)

↓
can be merged
in one state
(one row)

↓
can be merged in
one state
(one row)

	00	01	11	10
a, c, d	(a), 0	(a), 0	b, -	(d), 0
b, e, f	(b), 1	a, -	(b), 1	(e), 1

→

	00	01	11	10
a	(a), 0	(a), 0	b, -	(d), 0
b	(b), 1	a, -	(b), 1	(b), 1

⊕ It is ^{now} the time for state assignment, in state assignment we should try to avoid changes of more than one internal state variable when there is a change in the input (This is in order to avoid critical race).

In this example, there is no critical race because we have only 2 states (1 internal state variable x).

assign $a = 0$, $b = 1$

⇒ Transition table and output maps are

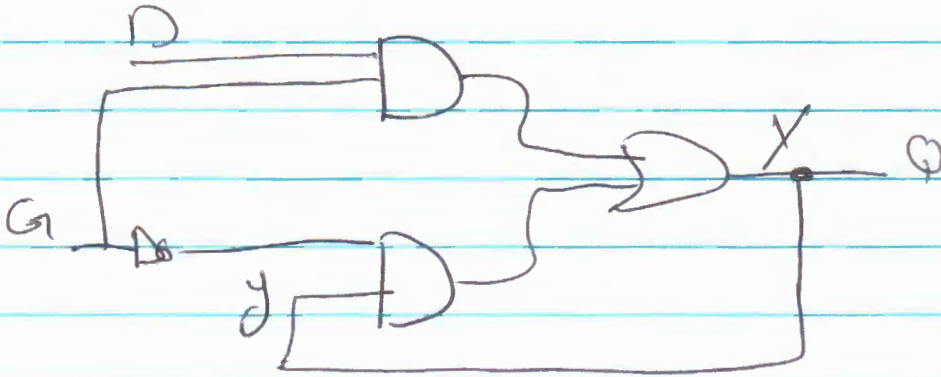
	DG			
y	00	01	11	10
0	0	0	1	0
1	1	0	1	1

$$Y = DG + G'y$$

	DG			
y	00	01	11	10
0	0	0	X	0
1	1	X	1	1

$$Q = y$$

$$\text{or } Q = X$$



⊗ map for S and R (using AND gate)

	DG			
y	00	01	11	10
0	0	0	1	0
1	X	0	X	X

$$S = DG$$

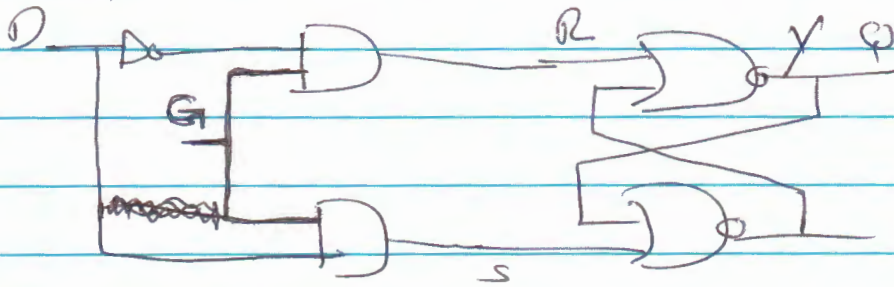
	DG			
y	00	01	11	10
0	X	X	0	X
1	0	1	0	0

$$R = D'G$$

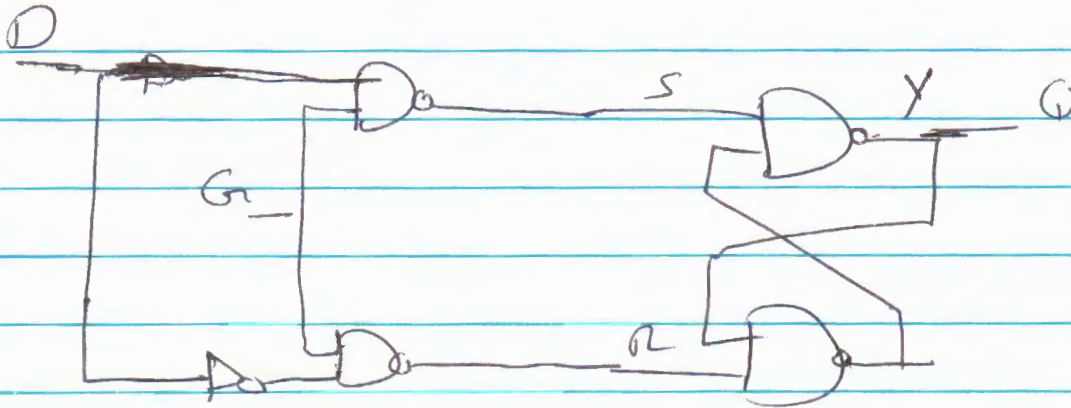
Q(t)	Q(t+1)	S	R
0	0	0	X
0	1	1	0
1	0	0	1
1	1	X	0

$$Y = DG + D'G$$

$$X = S + R'Y \quad (2 \text{ NAND gates})$$



$$Y = S' + RY \quad (2 \text{ NAND gates})$$



⊕ Assigning Outputs to Unstable states

- The stable states in a flow table have specific output entries associated with them
- The unstable states have unspecified output entries designated by dash (-)
- The output values for the unstable states must be chosen so that no momentary false outputs occur when the circuit switches between stable states ⇒ use the following rules

- ① Assign the output 0 when the transition occurs between two states with 0 outputs
- ② Assign the output 1 when the transition occurs between two states with 1 outputs
- ③ Assign ~~0~~ the output don't care (X) when the transition between two states with different outputs

Ex. given the following flow table

	0	1	
a	Ⓐ 0	b, -	0
b	c, -	Ⓑ 0	X
c	Ⓒ 1	d, -	1
d	a, -	Ⓓ 1	X

⇒

0	0
X	0
1	1
X	1

⊛ closed covering condition

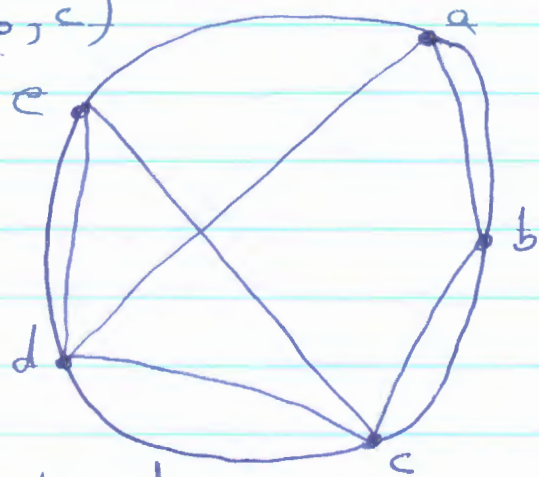
Example with implied states:

b	b, c ✓			
c	x	d, e ✓		
d	b, c ✓	x	a, d ✓	
e	x	x	✓	b, c ✓
	a	b	c	d

⇒ compatible pairs are
 (a, b) , (a, d) , (b, c) , (c, d) , (c, e) , (d, e)

⇒ maximal compatibles
 (c, d, e) , (a, b) , (a, d) , (b, c)

now ~~we~~ we want to find the ~~minimum~~ minimum set of compatibles that covers all the states and is closed



- (a, b) , (c, d, e) cover all states but it is not closed

closure table

compatibles	(a, b)	(a, d)	(b, c)	(c, d, e)
implied states	b, c	b, c	d, e	a, d b, c

⇒ A set of compatibles that will satisfy the closed covering condition is
(a,d) (b,c) (c,d,e)

⇒ We reduce the table from 5 rows to 3 rows.

- note that another closed-covered compatibles would be (a,b), (b,c), (d,e)
(In general, there may be more than one possible way of merging rows).

⊛ Race-Free state Assignment

- in our assignment for the states in a flow table we should prevent the occurrence of critical races.

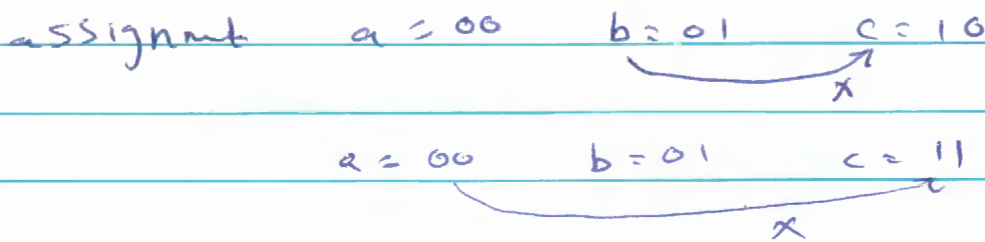
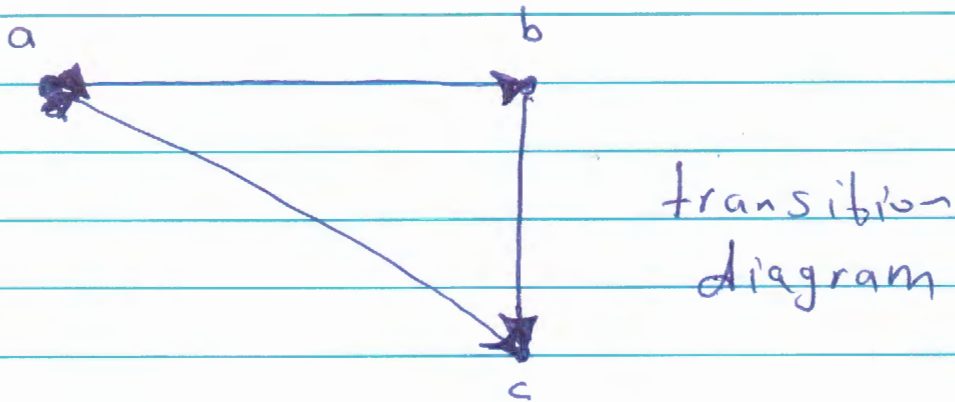
- critical races can be avoided if we assign the states such that only one change occur when we go from an internal state to another.

- In the Two-Row flow table we don't have any problems with assignments, but for 3-rows and more we may have some problems.

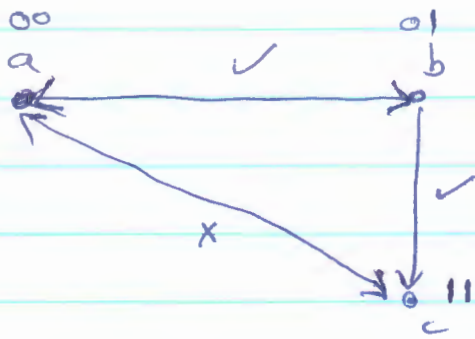
Example of 3-row flow table

	$x_1 x_2$			
	00	01	11	10
a	(a)	b	c	(a)
b	a	(b)	(b)	c
c	a	(c)	(c)	(c)

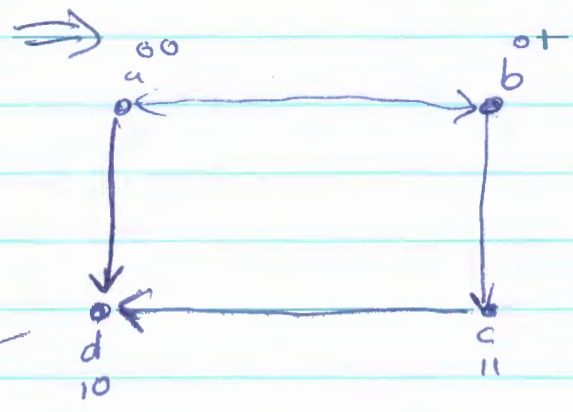
- The information inside flow-table about the transitions between states is transferred to a transition diagram



- This problem can be solved by adding a fourth row (d) to the flow table in order to form a cycle between stable states



from a to c critical
from c to a non-critical



slow transition
label

	00	01	11	10
a	a	b	d	a
b	a	b	b	c
c	d	c	c	c
d	a	-	c	-

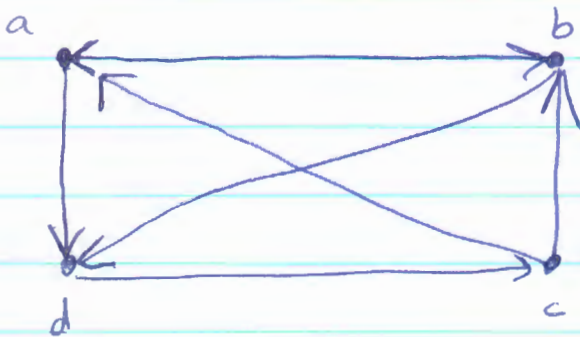
- the dashes in row d can be considered as don't care BUT must not be assigned a value of 10 because the state will be stable in this case.

Example of Four-Row flow table

$X_1 X_2$

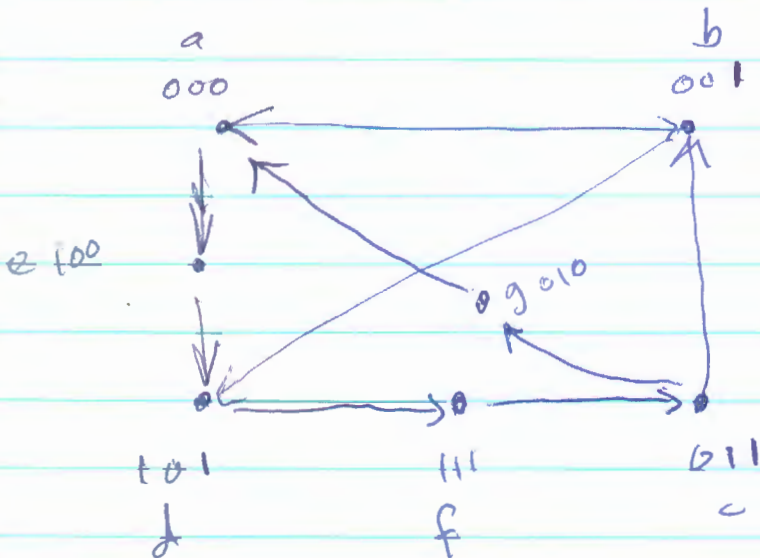
	00	01	11	10
a	b	(a)	d	(a)
b	(b)	d	(b)	a
c	(c)	a	b	(c)
d	c	(d)	(d)	c

⇒ transition table



00 01 11 10

000 001 | 011 010 | 110 111 | 101 100



2, 22

		00	01	11	10
a	000	b	(a)	e	(a)
b	001	(b)	d	(b)	a
c	011	(c)	g	b	(c)
g	010	-	a	-	-
-	110	-	-	-	-
f	111	c	-	-	c
d	101	f	(d)	(d)	f
e	100	-	-	d	-

H.W

9-2 , 9-4 , 9-6 , 9-9 , 9-12 , 9-14 ,
9-15 , 9-18 , 9-19 , 9-22 ,