



ADVANCED DIGITAL SYSTEMS DESIGN

HW_3_

Prepared By:

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1180180

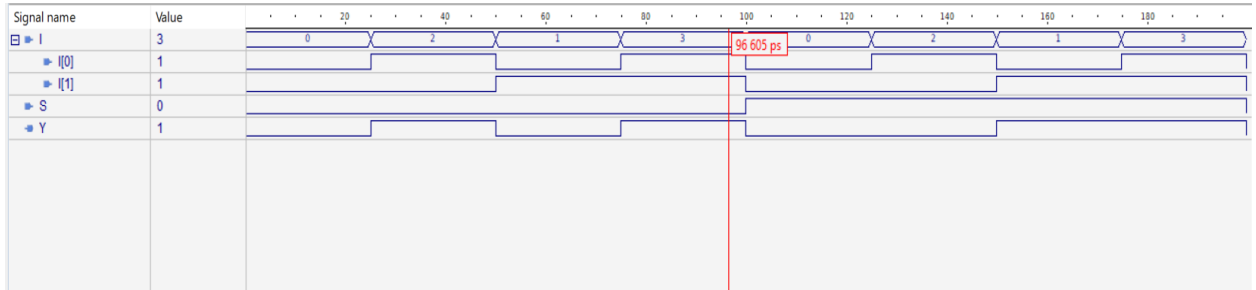
Instructor: Dr. Abdallatif Abuissa

Section: 1

Date: 30/10/2020

❖ Mux2x1:

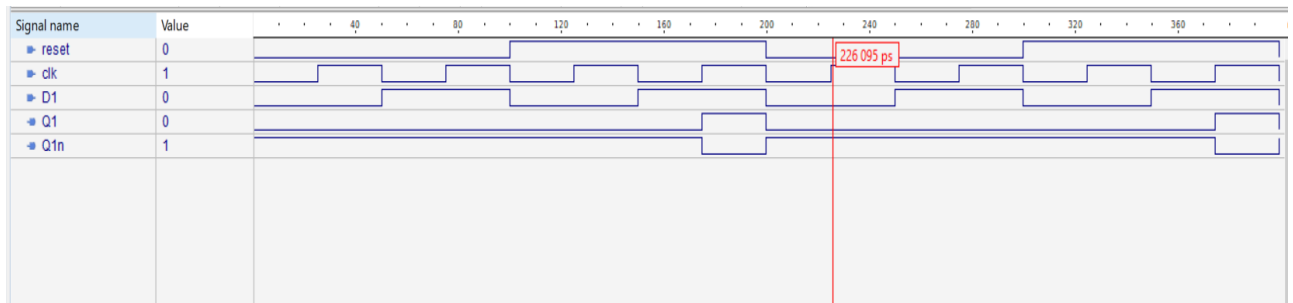
- Simulation:



Fig_1_(simulation of mux2x1)

❖ D flip_flop:

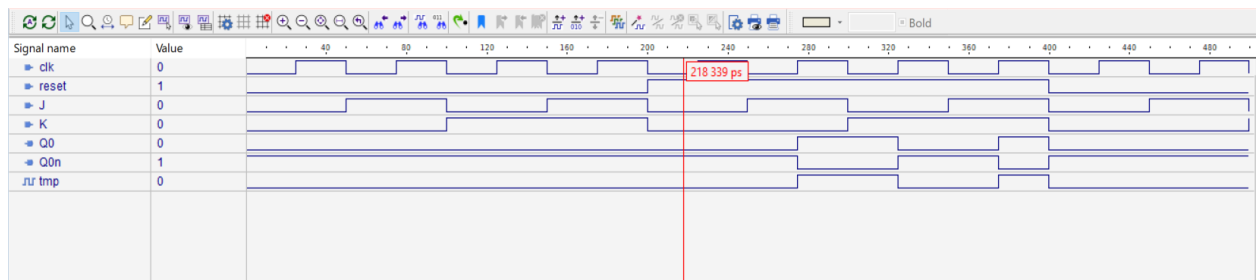
- Simulation:



Fig_2_(simulation of D flip_flop)

❖ JK flip_flop:

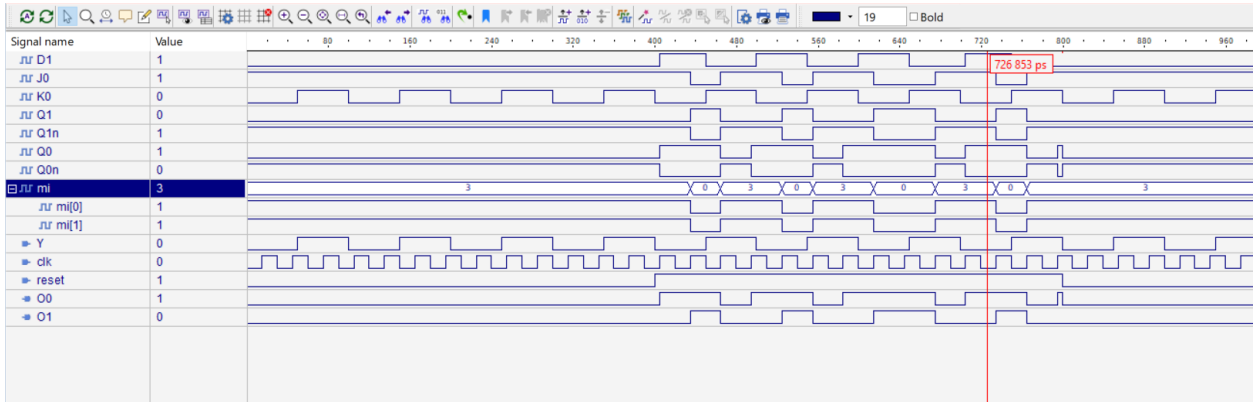
- Simulation:



Fig_3_(simulation of JK flip_flop)

❖ All Circuit structural:

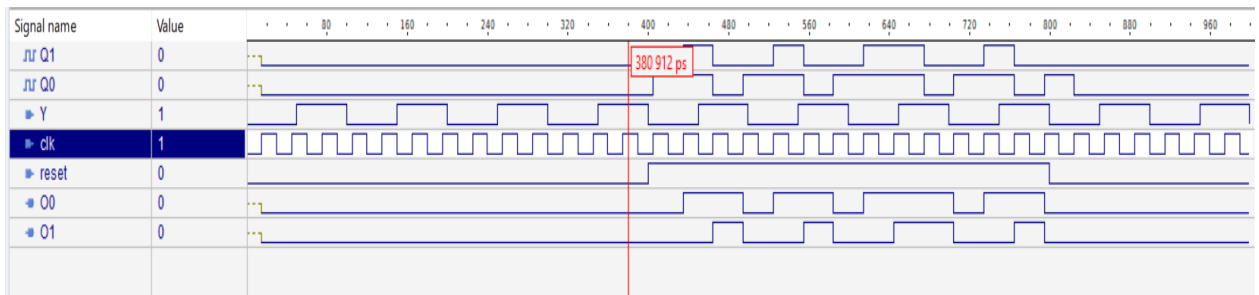
- Simulation:



Fig_4_(simulation of all circuit structural)

❖ All circuit behavioral:

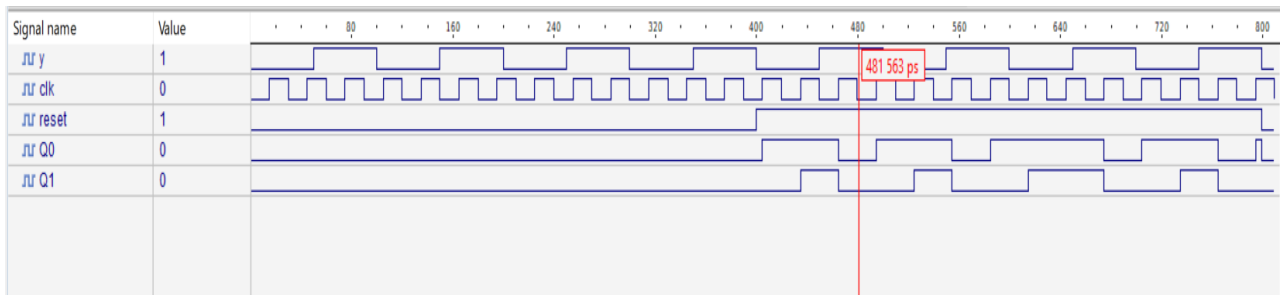
- Simulation:



Fig_5_(simulation of all circuit behavioral)

❖ All circuit test:

- Simulation:



Fig_6_(simulation of all circuit test)