

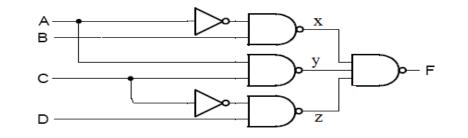
Faculty of Engineering and Technology Electrical and Computer Engineering Departmen Advanced Digital Design ENCS533 Homework#5

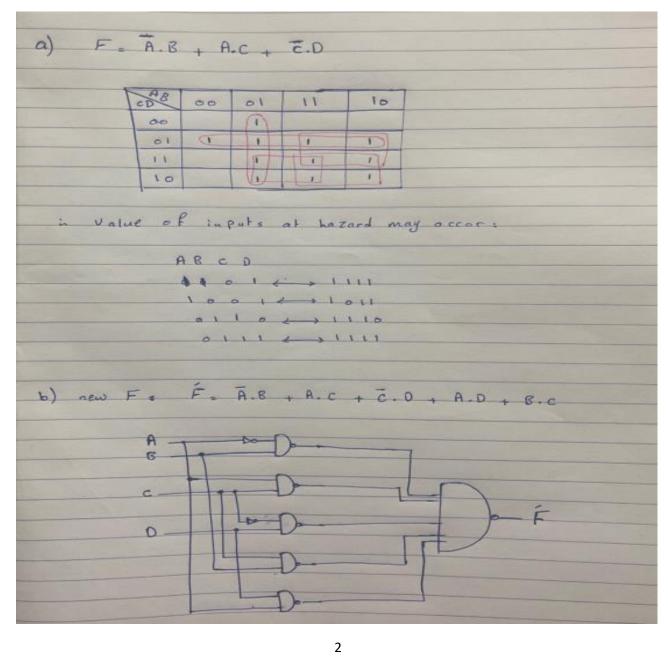
Prepared By: *Anas Nimer 1180180

Instructor: Dr. Abdallatif Abuissa Section: 1 Date: 21/2/2020 Q1) For the following circuit

i) Determine the values of inputs at which hazard may occur.

ii) Draw the hazard-free circuit (NAND-NAND implementation).





Q2) Use asynchronous design procedure to design a positive edge TFF that has 2 inputs T and CLK, and one output Q. Use SR Latches in your design.

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	G	1	1	0	V	after C.			
_	H	1	1	- 1	1	after D	01111		
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