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1. To have a complement CMOS we do not need

- A n-type transistors
- B p-type transistors
- C package



2. What are the different parts of MOS transistors?

- A Drain , Source & Gate and bulk
- B Source & Gate
- C Drain , Source
- D Drain , Source & Gate
- E bulk, Drain , Source



3. we can classify IC according to

A Signal Type



B Signal Name



C Analog or digital



4. The first transistor was created in

A Bell Labs



B China



C circuit design



5. Moore's law stated that the number of transistors in ICs doubles every ----- months

A 2 years



B 17 months



C 16 months



D 18 months



6. Technology shrinks or scaling, refers to which parameter of the transistor

A Drain



B source



C Gate length



D bulk



7. We need a ----- to run spice simulations

A device model



B power



C ground



D netlist



E all mentioned

8. in chip design, Wafer made of

A silicon



B Germanium



C copper



D None of mentioned



9. Die is larger than a waver

False



10. package is technology used to interface between die and outside word

True



11. CMOS stands for -----

- A** complementary material oxide state
- B** commuter material or silicon
- C** commuter metal organization states
- D** complementary metal oxide silicon
- E** Non of above



12. which sentences is true a bout Ntype and Ptype transistor

- A** they both made of silicon
- B** they both use as switch
- C** Ntype is slow
- D** ptype is faster than ntype
- E** Both use to build CMOS logic



Add a Question

Multiple Choice

True / False

Short Answer

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