

Chapter - 9 -

Fast Fourier Transform (FFT)
(Computation of DFT)

* FFT algorithms were a major landmark

- performance ~~improvement~~ improvement in computation.

$$(DFT) X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}$$

$$(IDFT) x(n) = \frac{1}{N} \sum_{k=0}^{N-1} X(k) W_N^{-nk}$$

Computation is mainly the same.

$$W_N = e^{-j \frac{2\pi}{N}}$$

so, * all methods used for computing DFT are also used for computing of IDFT.

Direct Computation:

general case is complex.

$$x(n) W_N^{kn} \Rightarrow \begin{matrix} 1 \text{ complex multiply} \\ (4 \text{ Mults, } 2 \text{ adds}) \\ \text{real} \quad \quad \quad \text{real} \end{matrix}$$

* to compute DFT for one value of (k), we need N Mults, and (N-1) adds.

$$X(k), k=0, 1, 2, \dots, N-1$$

$$N^2 \text{ complex mults}$$

$$N(N-1) \text{ complex adds}$$

$$\approx N^2 \text{ MADS}$$

Dependency on N^2

Fast DFT algorithms take advantage of symmetry of W_N^{kn} to reduce the computation to half N^2 or to quarter N^2 .

① Symmetry: $W_N^{k(N-n)} = W_N^{-kn} = (W_N^{kn})^*$

② Periodicity in (n) and (k): $W_N^{kn} = W_N^{k(n+N)} = W_N^{n(k+N)}$

①

* If $N=1000 \Rightarrow$ 1 million MADs
 Computation grows very fast as N grows to 2000, 3000, etc.

* Fast Fourier Transform (FFT).

$$N = P_1 \cdot P_2 \cdots P_n \quad (P_i \rightarrow \text{primes})$$

↓ can be expressed as product of primes

Complex MADs $\propto N [P_1 + P_2 + \dots + P_n]$
not to N^2 but

$$N = 2^{\log_2 N} \Rightarrow N \log_2 N$$

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{nk}$$

$$= \underbrace{\sum_{n \text{ even}} x(n) W_N^{nk}}_{n=2r} + \underbrace{\sum_{n \text{ odd}} x(n) W_N^{nk}}_{n=2r+1}$$

$$X(k) = \sum_{r=0}^{\frac{N}{2}-1} x(2r) W_N^{2rk} + \sum_{r=0}^{\frac{N}{2}-1} x(2r+1) W_N^{(2r+1)k}$$

$$W_N^{(2r+1)k} = W_N^k W_N^{2rk}$$

$$W_N^2 = e^{-j\frac{2\pi}{N}} = e^{-j\frac{2\pi}{N/2}} = W_{N/2}$$

So

$$X(k) = \sum_{r=0}^{\frac{N}{2}-1} x(2r) \underbrace{W_{N/2}^{rk}}_{\frac{N}{2} \text{ DFT}} + W_N^k \sum_{r=0}^{\frac{N}{2}-1} x(2r+1) \underbrace{W_{N/2}^{rk}}_{\frac{N}{2} \text{ DFT}}$$

combine to get $X(k)$

no need to be primes (just product of numbers)

The larger the number of primes and the smaller these numbers the higher the efficiency of FFT.

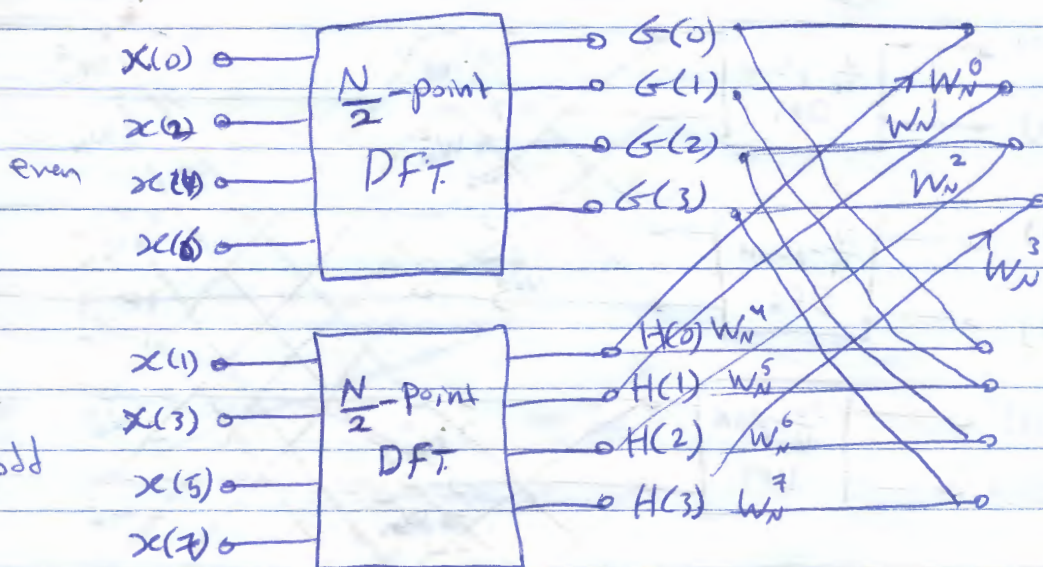
$$X(k) = \underbrace{\sum_{r=0}^{\frac{N}{2}-1} x(2r) W_{\frac{N}{2}}^{rk}}_{\substack{\frac{N}{2} \text{ point DFT} \\ G(k)}} + W_N^k \underbrace{\sum_{r=0}^{\frac{N}{2}-1} x(2r+1) W_{\frac{N}{2}}^{rk}}_{\substack{\frac{N}{2} \text{ point DFT} \\ H(k)}}$$

$$X(k) = \underbrace{G(k)}_{\substack{(\frac{N}{2})^2 \text{ MADS} \\ \frac{N}{2} \text{ point DFT}}} + W_N^k \underbrace{H(k)}_{\substack{(\frac{N}{2})^2 \text{ MADS} \\ \frac{N}{2} \text{ point DFT}}}$$

N -point DFT $\propto N^2$ MADS

So, computation is $2 \left(\frac{N}{2}\right)^2 + N = N + \frac{N^2}{2}$ MADS.

* in computing $G(k)$ ($k \rightarrow 0 \rightarrow \frac{N}{2}-1$), then we use these values for the other values $\frac{N}{2} \rightarrow N-1$ because $G(k)$ is periodic in k .



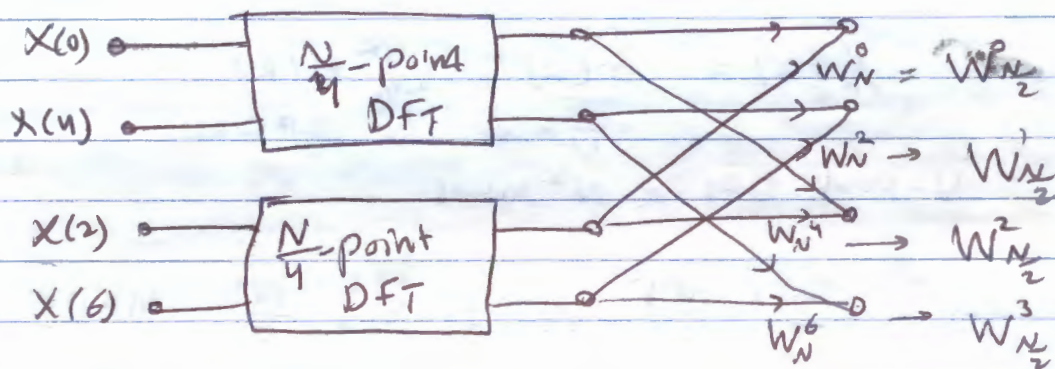
$$X(k) = G(k) + W_N^k H(k)$$

completing $G(4) = G(0)$, $G(k+4) = G(k)$
 $G(k) \rightarrow$ periodic in k . $H(k+4) = -H(k)$

$$2 \left(\frac{N}{2}\right)^2 + N \text{ MADS} < N^2 \text{ MADS}$$

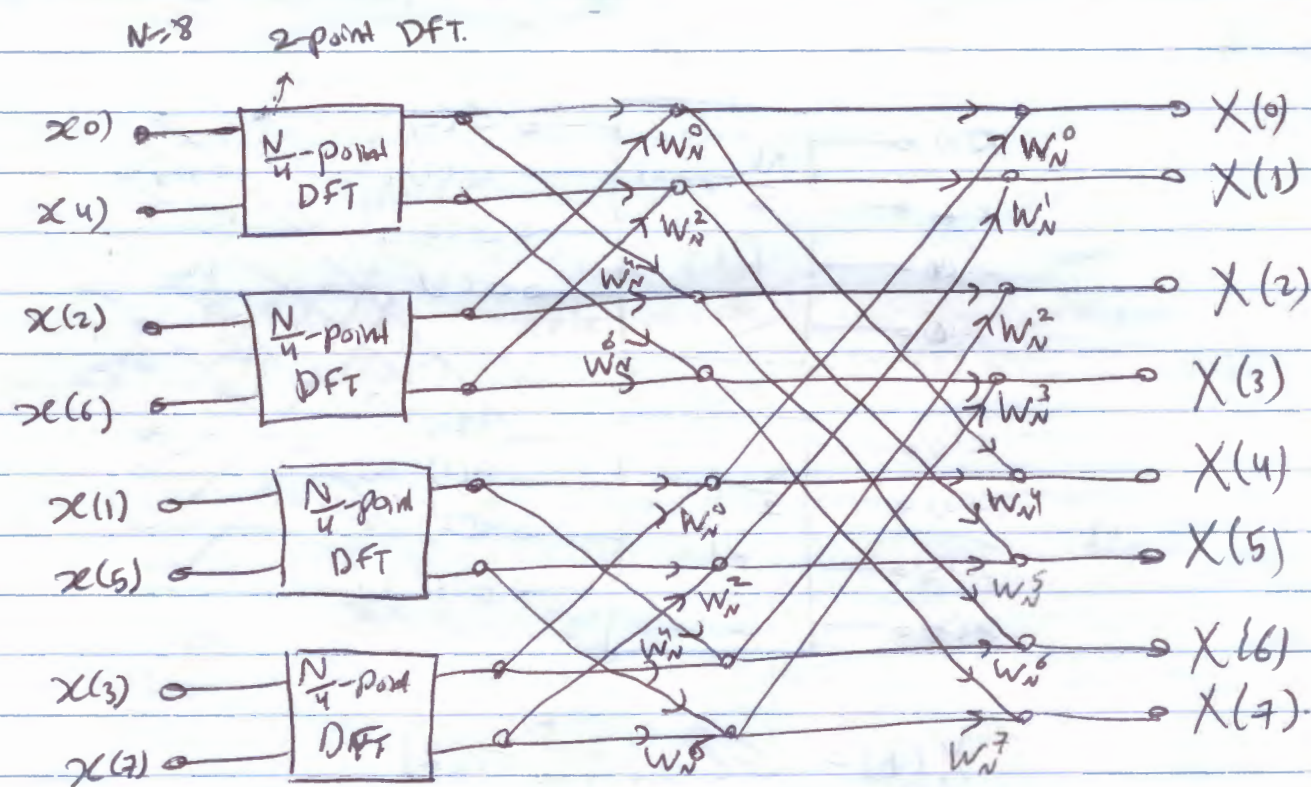
(3)

Now, $\frac{N}{2}$ -point DFT is direct computation
 but, if $\frac{N}{2}$ is a composite number, then we can
 apply the same idea on $\frac{N}{2}$ -point DFT.

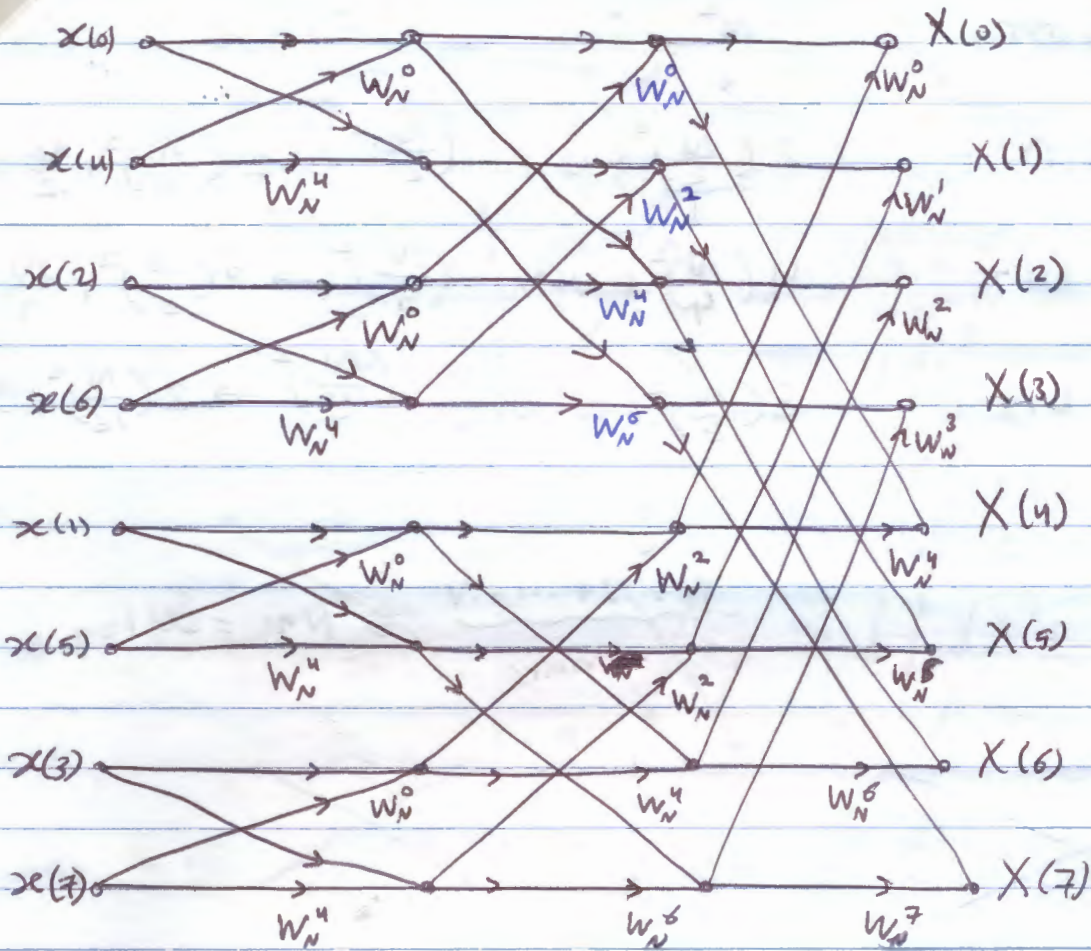
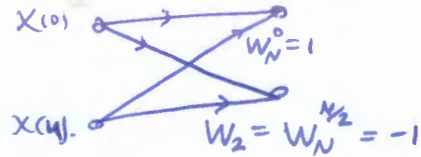


see ①

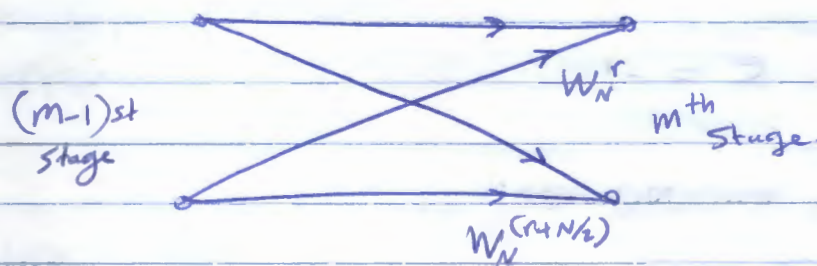
$$W_{\frac{N}{2}} = W_N^2$$



2-point DFT



* Basic butterfly computation (flow graph).



By counting branches with transmittance of W_N^r , we note that each stage has N complex mult. and N complex Add.

Since, we have $N \log_2$ stages, we have in total $N \log_2 N$ complex MADS.

e.g. if $N = 1024 = 2^{10} \Rightarrow N^2 = 2^{20} = 1,048,576$

$N \log_2 N = 10,240$

reduction of more than two orders of magnitude.

$$N = 2^2$$

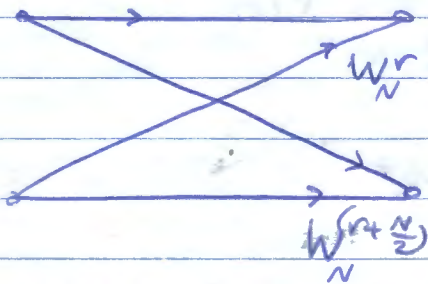
- | | | | |
|---|--------------------------|--|--|
| 1 | N-point DFT | N^2 | \rightarrow replaced by $2 \left(\frac{N}{2}\right)^2 + \frac{N}{2}$ |
| 2 | $\frac{N}{2}$ -point DFT | $2 \left(\frac{N}{2}\right)^2 + N$ | $\left(\frac{N}{2}\right)^2 \rightarrow 2 \left(\frac{N}{4}\right)^2 + \frac{N}{2}$ |
| 4 | $\frac{N}{4}$ -point DFT | $4 \left(\frac{N}{4}\right)^2 + N + N$ | $\left(\frac{N}{4}\right)^2 \rightarrow 2 \left(\frac{N}{8}\right)^2 + \frac{N}{4}$ |
| 8 | $\frac{N}{8}$ -point DFT | $8 \left(\frac{N}{8}\right)^2 + N + N + N$ | $\left(\frac{N}{8}\right)^2 \rightarrow 2 \left(\frac{N}{16}\right)^2 + \frac{N}{8}$ |

\vdots
 \downarrow
 $\underbrace{N + N + \dots + N}_{\text{times}}$

$\Rightarrow NV = N \log_2 N$

see ②

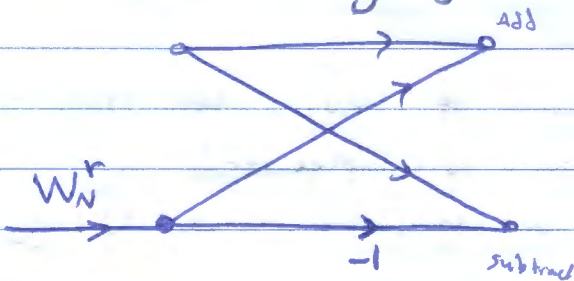
butterfly



$$W_N^{(r+N/2)} = W_N^r W_N^{N/2}$$

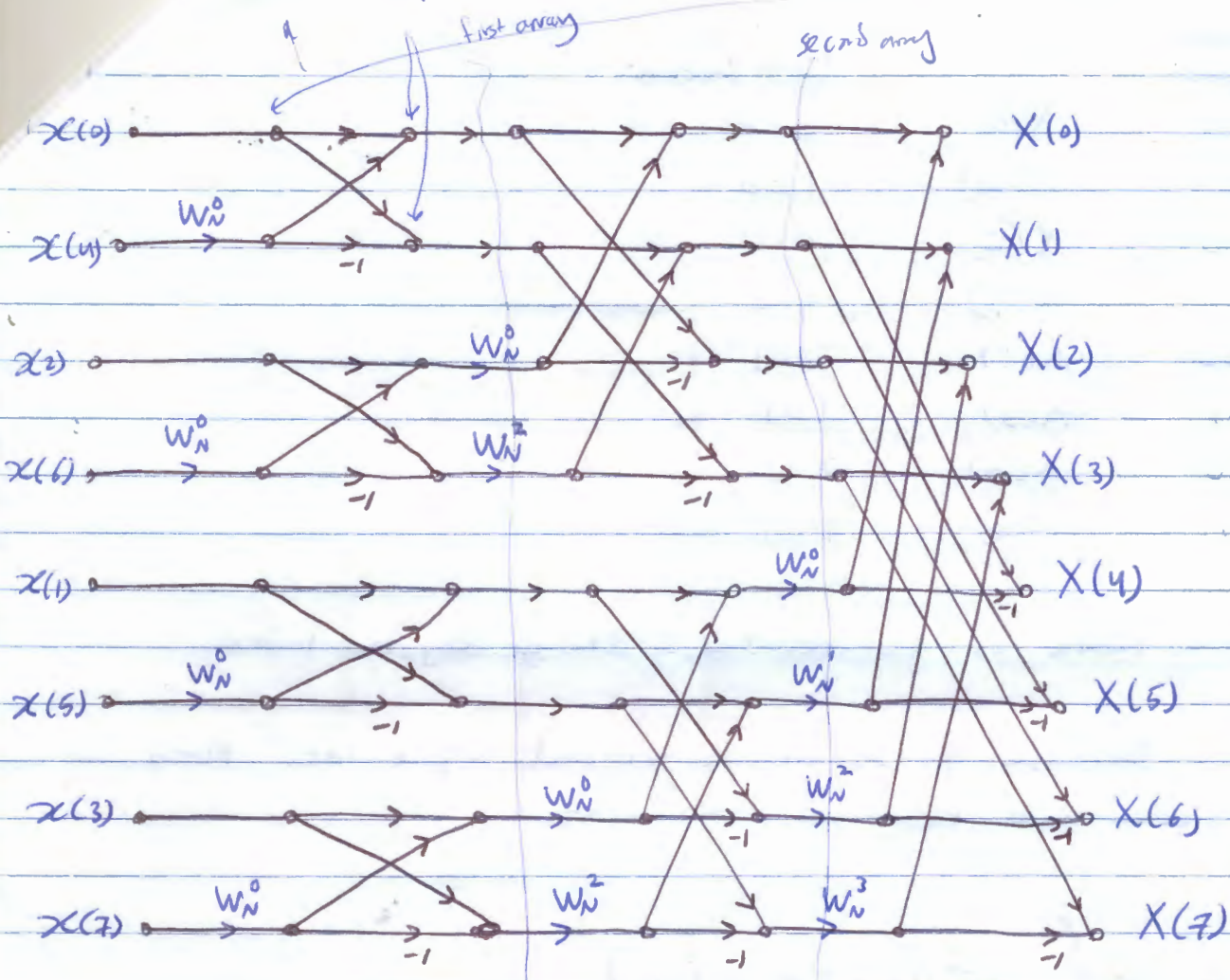
$$W_N^{N/2} = e^{-j \frac{2\pi}{N} \frac{N}{2}} = e^{-j\pi} = -1$$

replace basic butterfly by this network:



by this, we reduce multiplications by factor of 2

Don't need it later. depend on Sn_1 and Sn_2 (so, we can update input memory location)



This is one algorithm for computing FFT (DFT) (decimation in time) (flow-graph form)

Some notice points:

* Input seq. re-arrange \rightarrow bit-reversed order of binary representation of indices. \rightarrow how to arrange memory storage.

- In-place computation (we can use the same array for input, first array, second array and output array)

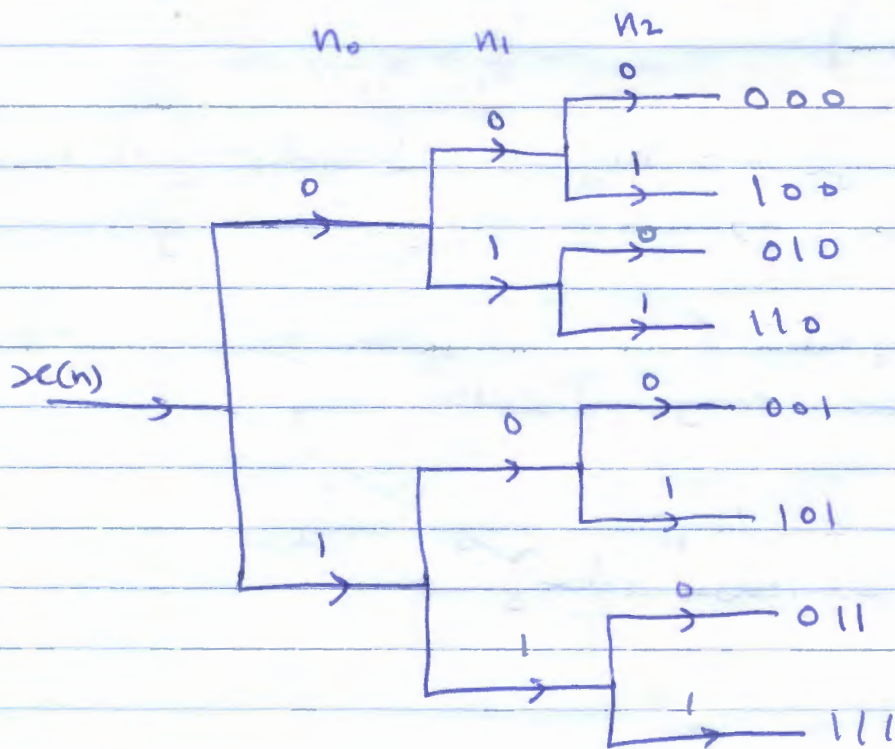
{ so, this algorithm is an in-place algorithm }
 { input should be in a bit-reversed order }

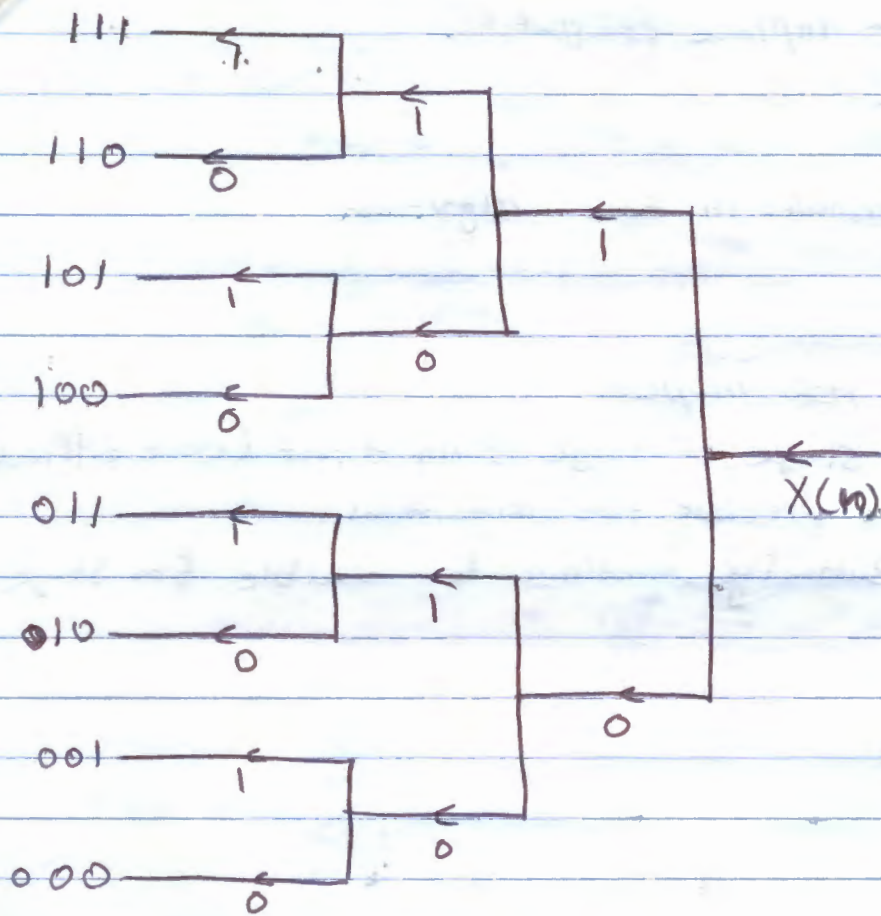
	Storage Register		Data Index
0	000	X(0)	000
1	001	X(4)	100
2	010	X(2)	010
3	011	X(6)	110
4	100	X(1)	001
5	101	X(5)	101
6	110	X(3)	011
7	111	X(7)	111

* Data Index is bit reversed of Storage Register Index.

* Reason, because if we divide original input into even and odd and then even into even and odd ... etc

$$x(n) = x \left[\dots 2^2 n_2 + 2^1 n_1 + 2^0 n_0 \right]$$





* Sorting input data into bit-reversed order \Rightarrow outputs Normal Index ^{order}.

* back to final flow-graph of FFT algorithm, to keep input data in normal order, we can re-order the horizontal lines in the graph to get inputs in normal order. But in this way the order of outputs will be bit-reversed order.

$x(0)$ _____

$x(1)$ _____

$x(2)$ _____

$x(3)$ _____

$x(4)$ _____

$x(5)$ _____

$x(6)$ _____

$x(7)$ _____

$X(0)$

$X(4)$

$X(2)$

$X(6)$

$X(1)$

$X(5)$

$X(3)$

$X(7)$

(9)

* This is not in-place computation algorithm

- * Input - normal order \Rightarrow output \rightarrow normal order
- * Computation is no longer in-place computation
- * butterflies are distorted.

+ This is the formal Decimate-in-time algorithm.

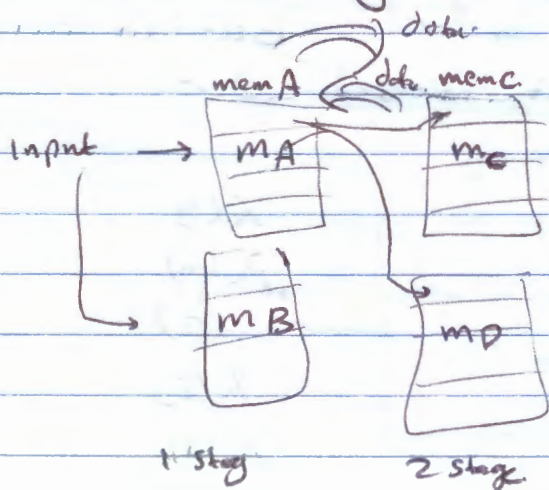
Disadvantages

- ① Computation is not in-place.
- ② Indexing from stage to stage is not direct (more difficult) compared with the previous two algorithms.
- ③ Height or width of Butterfly continue to double from stage to stage.

- RAM is required

* New algorithm (sequential memory is used rather than Random access memory) (Shift Reg. memory).

- arrangement of stages is identical.
(Indexing is identical in all stages).



* Flush data into memA and memB then into memC and memD. Then flush back data from memC and memD into memA and memB.

Four separate seq. memories

[not in-place computation] (10)

* Decimation in Frequency

* instead of separating input points into even and odd numbers, we compute DFT for even output points and odd output points

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{nk} \rightarrow \text{two}$$

$$= \sum_{n=0}^{\frac{N}{2}-1} x(n) W_N^{nk} + \sum_{n=\frac{N}{2}}^{N-1} x(n) W_N^{nk}$$

$$W_N^{\frac{N}{2}k} \sum_{n=0}^{\frac{N}{2}-1} x(n + \frac{N}{2}) W_N^{nk}$$

$$X(k) = \sum_{n=0}^{\frac{N}{2}-1} \left[x(n) + (-1)^k x(n + \frac{N}{2}) \right] W_N^{nk}$$

looks like $\frac{N}{2}$ -point DFT, but it is not because W_N^{nk}

Let's look at these output points when even & odd

k even - $X(2r)$

$$\sum_{n=0}^{\frac{N}{2}-1} \left[x(n) + x(n + \frac{N}{2}) \right] W_N^{2rn}$$

$r = 0, 1, 2, \dots, \frac{N}{2}-1$

k, odd - $X(2r+1)$ / $r = 0, 1, 2, \dots, \frac{N}{2}-1$

$$\sum_{n=0}^{\frac{N}{2}-1} \left[x(n) - x(n + \frac{N}{2}) \right] W_N^n W_N^{2rn}$$

$$W_N^{2rn} = W_{\frac{N}{2}}^{rn}$$

- k even

$$X(2r) = \sum_{n=0}^{\frac{N}{2}-1} g(n) W_N^{rn} \quad , \quad \frac{N}{2}\text{-point DFT}$$

Where, \nearrow first half of data part \nearrow last half of input points

$$g(n) = x(n) + x\left(n + \frac{N}{2}\right)$$

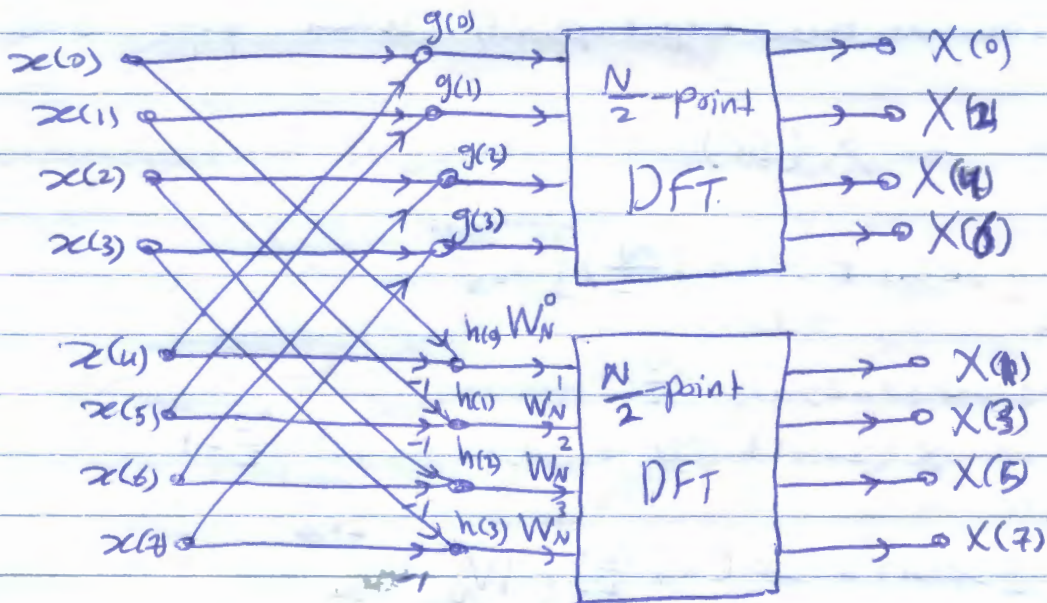
- k odd

$$X(2r+1) = \sum_{n=0}^{\frac{N}{2}-1} \{ h(n) W_N^{rn} \} W_N^{\frac{r}{2}}$$

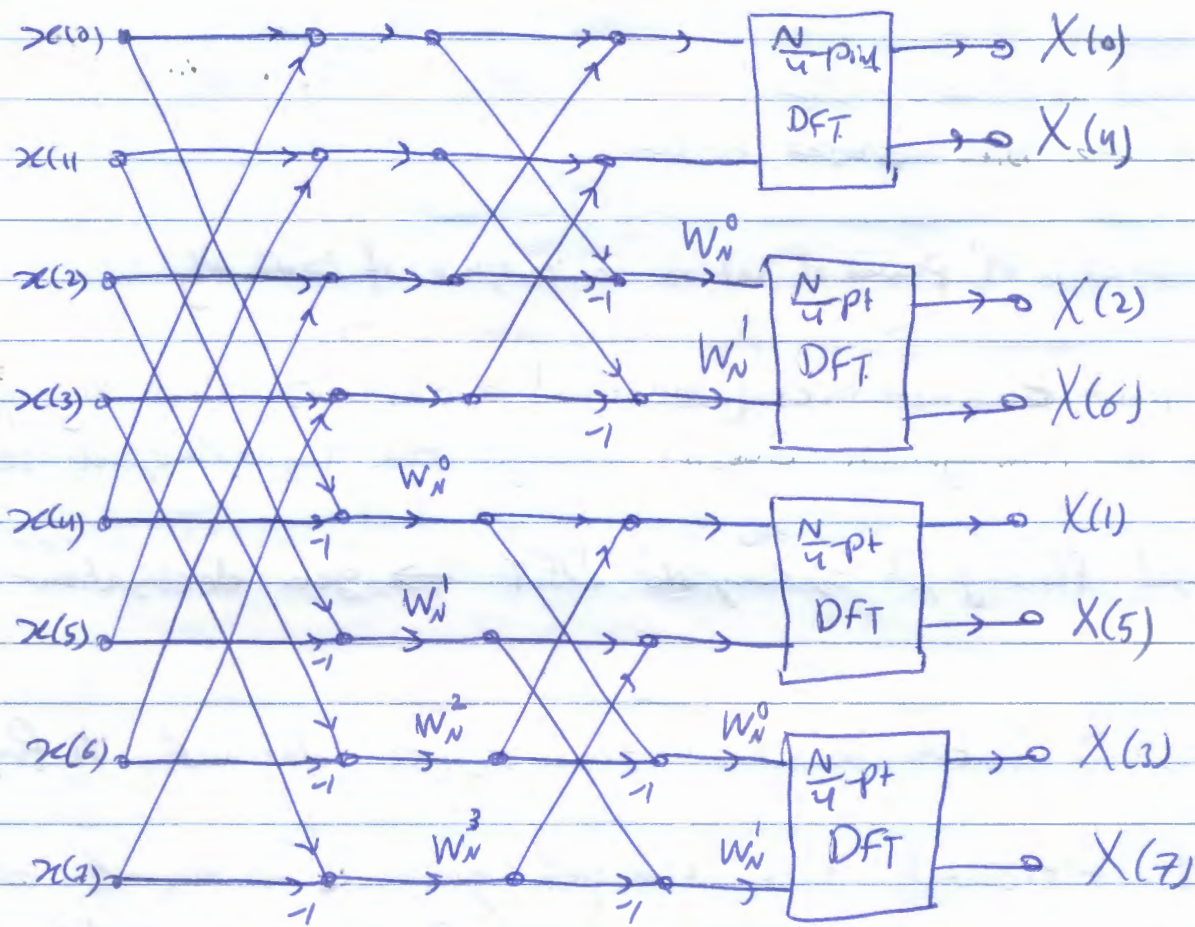
where

$$h(n) = \underbrace{x(n)}_{\text{first half}} - \underbrace{x\left(n + \frac{N}{2}\right)}_{\text{second half}}$$

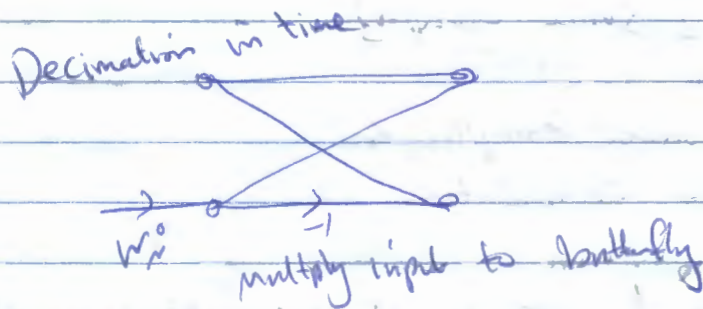
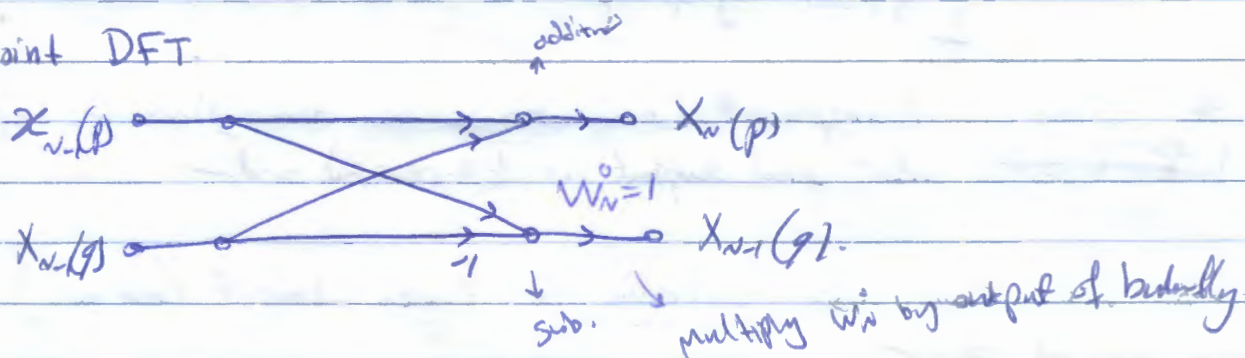
of computations (MADS) same as decimade in time.



Similar to DST, decompose $\frac{N}{2}$ -point into $\frac{N}{4}$ -point



* 2-point DFT



* show final flow graph for decimation in freq.

decimation in Freq. - - - Continue

data input \rightarrow normal order
output \rightarrow bit-reversed order.

\rightarrow multiplication of powers of W is on output of butterfly

* Note.

one of flow-graph characteristics (mentioned earlier) that by applying transposition on flow-graph doesn't change the input/output relationship.

\rightarrow Transposed flow-graph \Rightarrow ^{also} computes DFT. \rightarrow get decimation-in-time.

Transposition of ~~the~~ decimation-in-time \Rightarrow get decimation-in-freq.

\Rightarrow sort horizontal line to get outputs in normal order
inputs are in bit-reversed order (In-place computation)
[show graph]

* This is transposed (decimation-in-time) when input is in ~~bit-reversed~~ ^{normal} order and output in bit-reversed order.

* We can also arrange it to get input (normal) and output in normal order.

[show graph]

- Indexing is more complicated.

- not in-place computation.

* final re-arrangement of decim. in freq. [sequential memory]

[show graph]

- Index is straightforward (identical from stage to stage)
- input (Normal), output bit-reversed order

- we ^(disk, ROM, tape, shift Reg.) sequential memory, with no need for RAM.

- Four memories: A B C D

store first half in A
 " second " " B

first point in mem. A

" " " " B → store in mem. C location 1 and location 2

* some computational issues: (considerations).

① Inverse DFT

$$x(n) = \frac{1}{N} \sum_{k=0}^{N-1} X(k) W_N^{-nk}$$

DFT:

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}$$

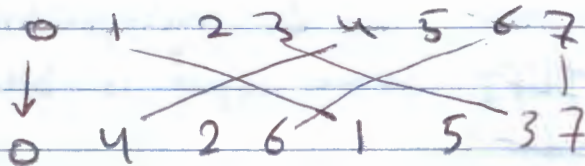
Complex Conjugate of

To use algorithm (DFT for IDFT)

* $\frac{1}{N} \rightarrow$ scaling

* W_N^{-kn} → conjugate powers of W (Conj or flipping input $\rightarrow x(-n)$)

② bit-reversal (in-place computation) (no need for double spec)



Swap ^{plac.}
 1, 4
 3, 6

⑮

* Implementing bit-reversal in HW, we just re-arrange the wires.

* To Implement bit-reversal in Index register X

third comp. Issue

③ Dealing with Coefficients

① store them in table.

② generating coeff. as we need them (recursively).
power of W changes from stage to stage.

in FFT, coeff are accessed in normal order

[show dec. in freq. , input-normal, output-bit-reversal]
 $W_N \rightarrow$ normal order)

[show dec. in time] [input-normal, output, bit-reversal]

$W_N \rightarrow$ bit-reversal order

* Coefficients \rightarrow stored in normal order or bit-reversal order.

* To use FFT for computing Convolution or Correlation
we find transform of sequences, multiply in freq. domain ^{with help}
and then, we take IDFT of the product.

Now, in this case, we can avoid the bit-reversed order.

For example, we can choose algorithm for DFT where input is in normal order and output in bit-reversed order, and assume impulse response is stored in bit-reversed order, we do multiplication, and we choose an algorithm for IDFT, where input is bit-reversed order and output in normal order.

For DFT

— decimation-in-time [normally order input, bit-reversed output]
Coefficients (W_N) are in bit-reversed order

For IDFT

— decimation-in-freq. [bit-reversed input, normally ordered output]
Coeff. (W_N) are in normal order

⇒ To match ^{up} Coeff. W_N order. [deci. in-time and deci. in-freq.]

For — decimation in freq. [normally input, output bit-reversed order]
 $W_N \rightarrow$ normal order

— decimation-in-time [input is in bit-reversed order
outputs are in normal order]

W_N Coefficients are also in normal order.

all of previous are
called

radix-2 FFT algorithms

* general radix FFT

$$N = P_1 \cdot P_2 \cdots P_r$$

$$N = P_1 \cdot 2_1$$

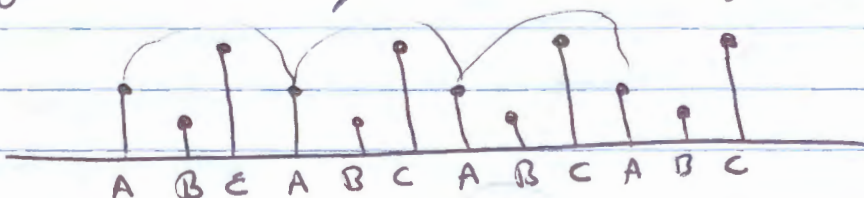
decompose seq. into;

subsequence: every P_1^{th} point. [paralling decimation-in-time]

P_1 sequences of length 2_1

$$N = 12, P_1 = 3$$

generate 3 subsequences of length 4, every 3 point. chosen.



A AAA, BBBB, cccc,

$$X(k) = \sum_{n=0}^{N-1} x(n) W_N^{kn}$$

$$= \sum_{r=0}^{q_1-1} x(Pr+r) W_N^{Pirk} + \sum_{r=0}^{q_1-1} x(Pr+1) W_N^{(Pr+1)k}$$

$$= \sum_{l=0}^{P_1-1} W_N^{lk} \sum_{r=0}^{q_1-1} x(Pr+l) W_N^{Pirk}$$

$$W_N^{Pirk} = e^{j \frac{2\pi}{N} P_1 rk} = e^{j \frac{2\pi}{q_1} rk} = W_{q_1}^{rk}$$

$$X(k) = \sum_{l=0}^{P_1-1} W_N^{lk} \underbrace{\sum_{r=0}^{q_1-1} x(Pr+l) W_{q_1}^{rk}}_{q_1\text{-point DFT}}$$

$$q_1 = P_2 \cdot \underbrace{P_3 \dots P_n}_{q_2} \Rightarrow \text{we proceed}$$

MADS \Rightarrow

$$N [P_1 + P_2 + \dots + P_n - n]$$

* Radix-2 algorithms are the most efficient algorithms, and to make N as 2^n we can augment it with zeros to make a power of 2.