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**BIRZEIT UNIVERSITY**  
Electrical Engineering Department  
Electronics – ENEE236

First Exam

November 1, 2011

ID:

Section: 1091496

**Question 1 (12 points)**

Given the Zener voltage regulator of Figure 1, where  $V_Z = 10V$  (measured at  $I_Z = 25mA$ )

1. Find the minimum and maximum possible values of  $V_i(t)$  in order to have a load voltage  $V_L = 10V$  without exceeding the ratings of the zener diode
2. Find the minimum and maximum value of load voltage  $V_L$  knowing that  $I_{Z(min)} = 5mA$  and  $r_z = 10\ \Omega$

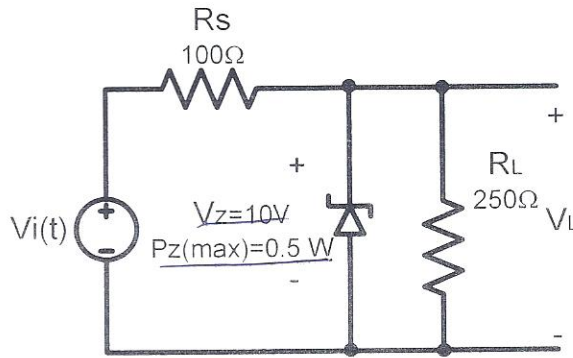


Figure 1

**Question 2 (10 points)**

Refer to Figure 2 to design a **diode-clamper** circuit that produces the output voltage  $V_o(t)$  if the input voltage  $V_i(t)$  is given as shown. Analyze your proposed design to make sure it does the job right

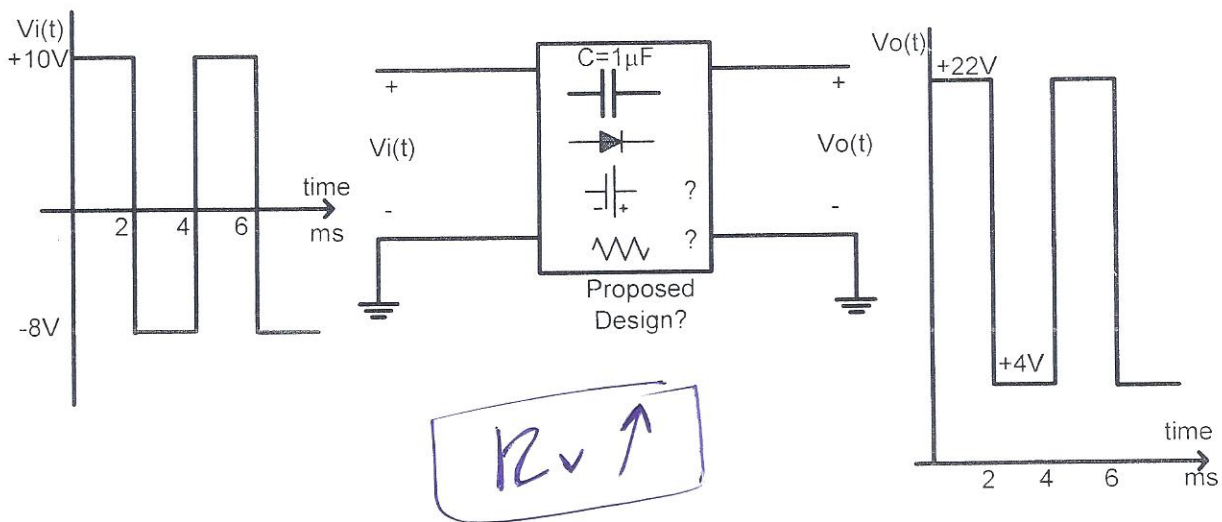


Figure 2

*Adham Sultan*

**Question 3 (8 points)**

Refer to the Common-Base Amplifier circuit of Figure 3 answer the following

- 1) Find value of  $h_{ib}$  ( assume  $V_T=26mV, \alpha=0.99$ )
- 2) Draw the ac small signal equivalent circuit and find expression for the voltage gain  $A_v$

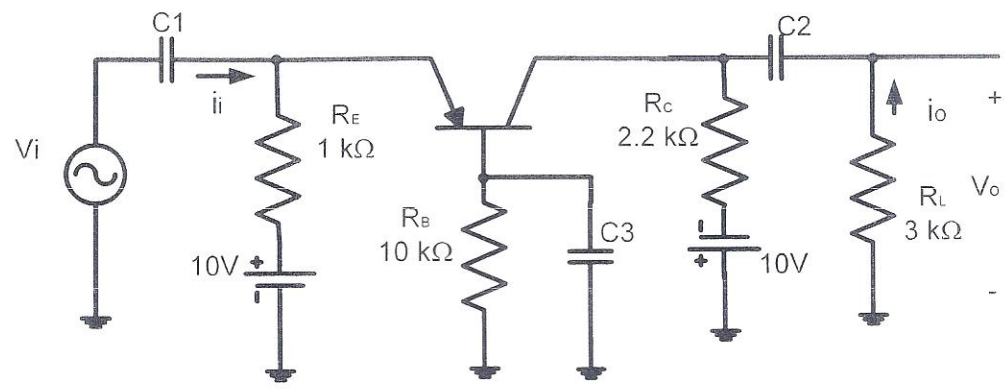


Figure 3

**Question 4 (20 points)**

The transistor amplifier of Figure 4 has two outputs:  $V_{o1}$  (CE) and  $V_{o2}$  (CC) . assuming that

$\beta=120, V_T=26mV$

- (a) Calculate the value of  $h_{ie}$
- (b) Find  $i_{c(max)}$  and  $V_{ce(max)}$  and draw the ac load line
- (c) Calculate the voltage gain  $A_{v1}=V_{o1}/V_i$
- (d) Calculate the current gain  $A_{i2}=i_{o2}/i_i$
- (e) Calculate the input impedance  $Z_i$
- (f) Calculate the output impedances  $Z_{o1}$  and  $Z_{o2}$

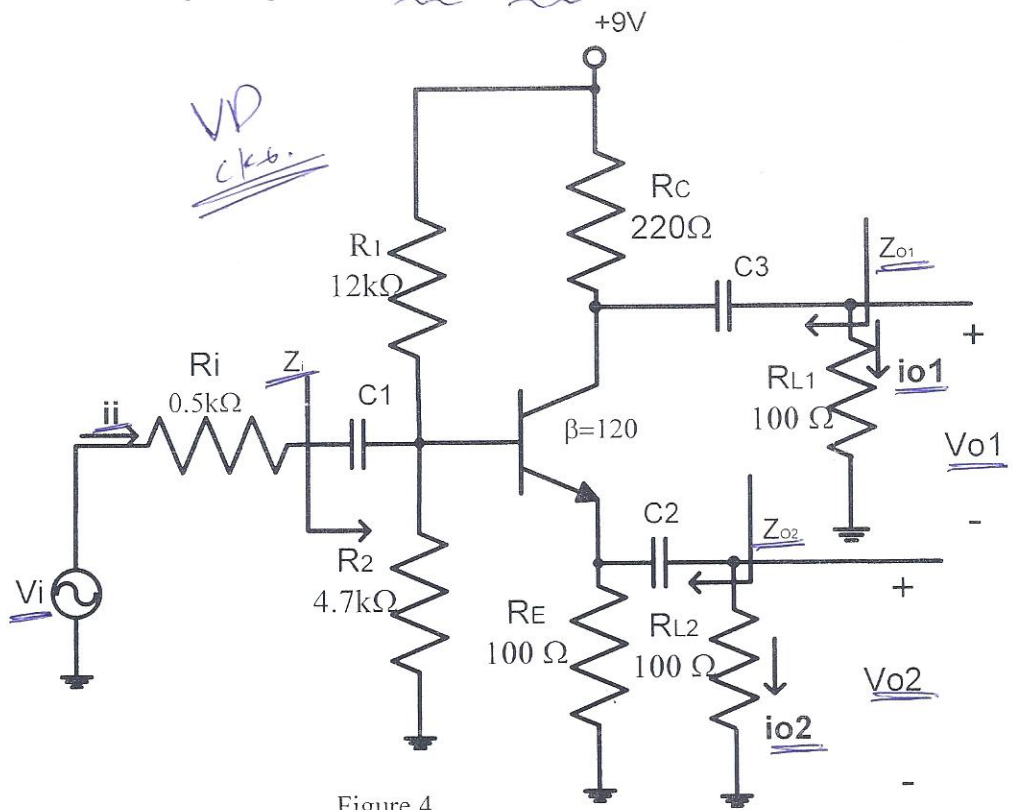


Figure 4

*Bon Chance  
Nasser Ismail  
Fall 2011*