

Faculty of Engineering and Technology

Electrical and Computer Engineering Department

ENEE3102

Electronics Lab

Experiment # 10

Pre lab

 *Student’s name : Juomah Bader*

 *Student’s number : 1162462*

*Instructor : Dr.Mohammad AL-Jubeh*

*I. ZENER DIODE :*

**A.**



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1. VR (V) :



1. VZ (V) :



1. I (mA) :



**B.**



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1. VR (V) :



1. VZ (V) :



1. I (mA) :



**C.** 1.



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****

2.



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*II. THE VOLTAGE REGULATED POWER SUPPLY :*

1. 1. When RL is open :

Io = 0



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2. with RL :



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Vo :



Io :



1. 1. When RL is open :

Io = 0 And : Vo = 9.291 V



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2. with RL :



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Vo :



Io :

