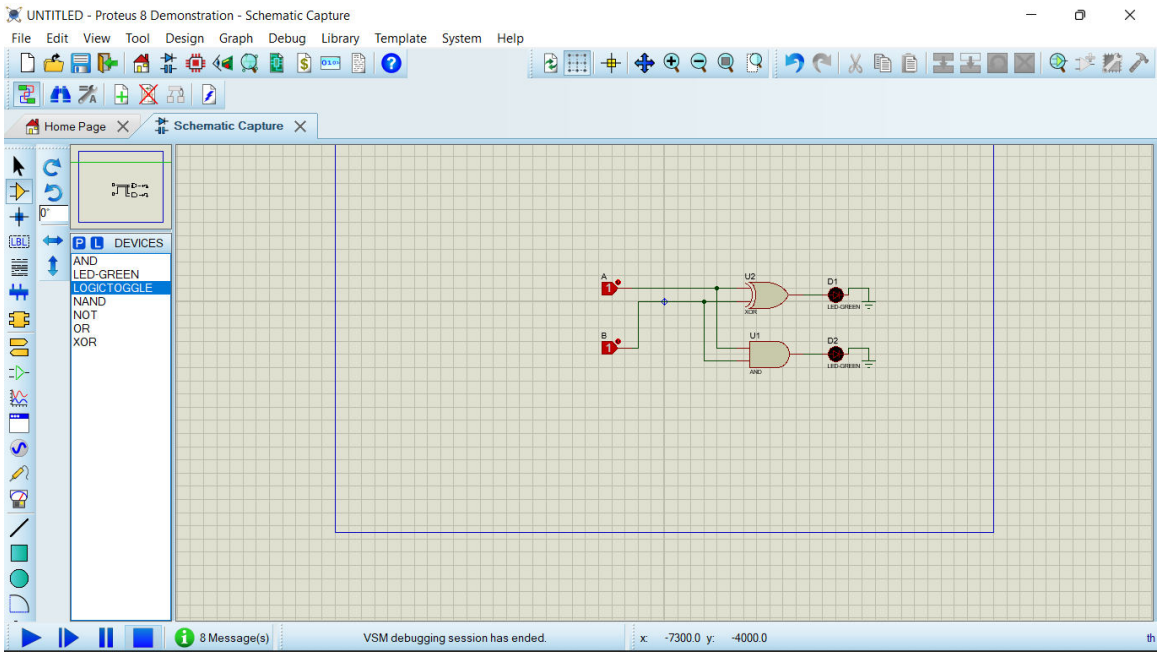
name : Asmaa Abed Al-Rahman Fares

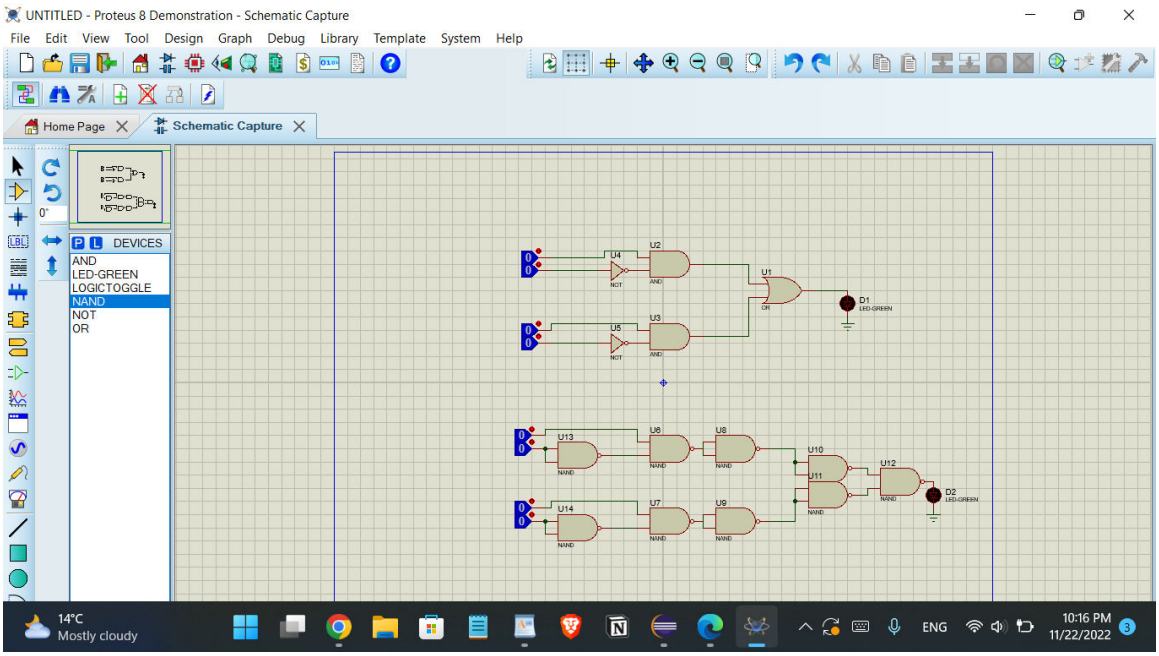
Std.no:1210084

prelab 4

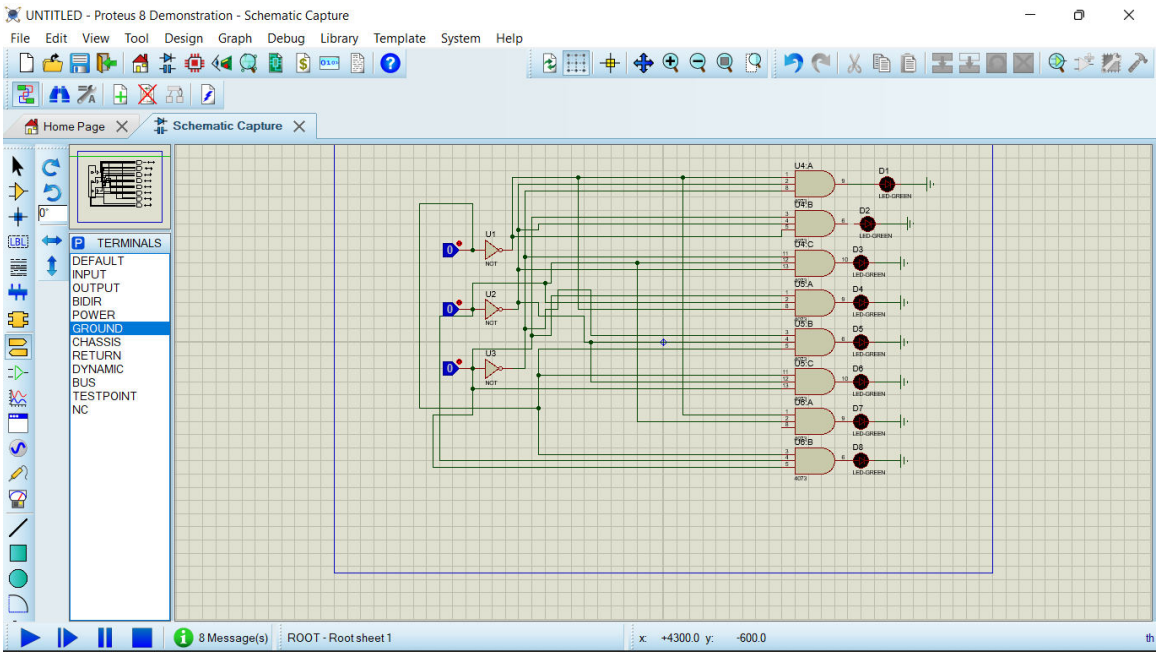
a) Build Full Adder using basic gates.



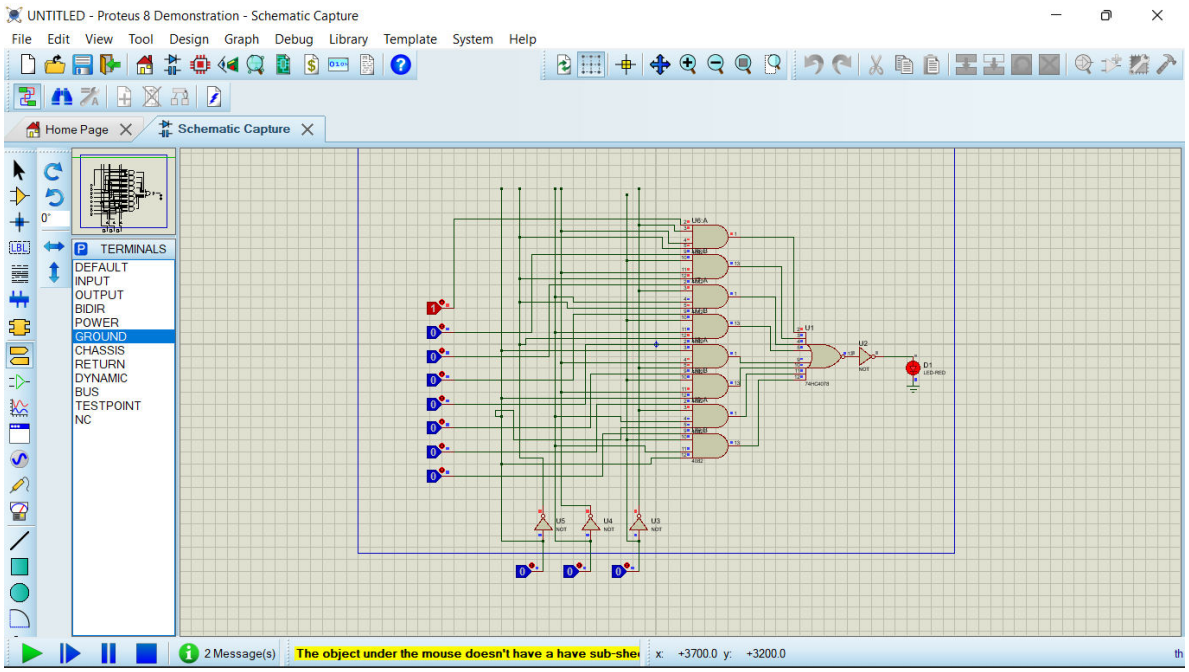
b) Build the bellow circuit using universal gates



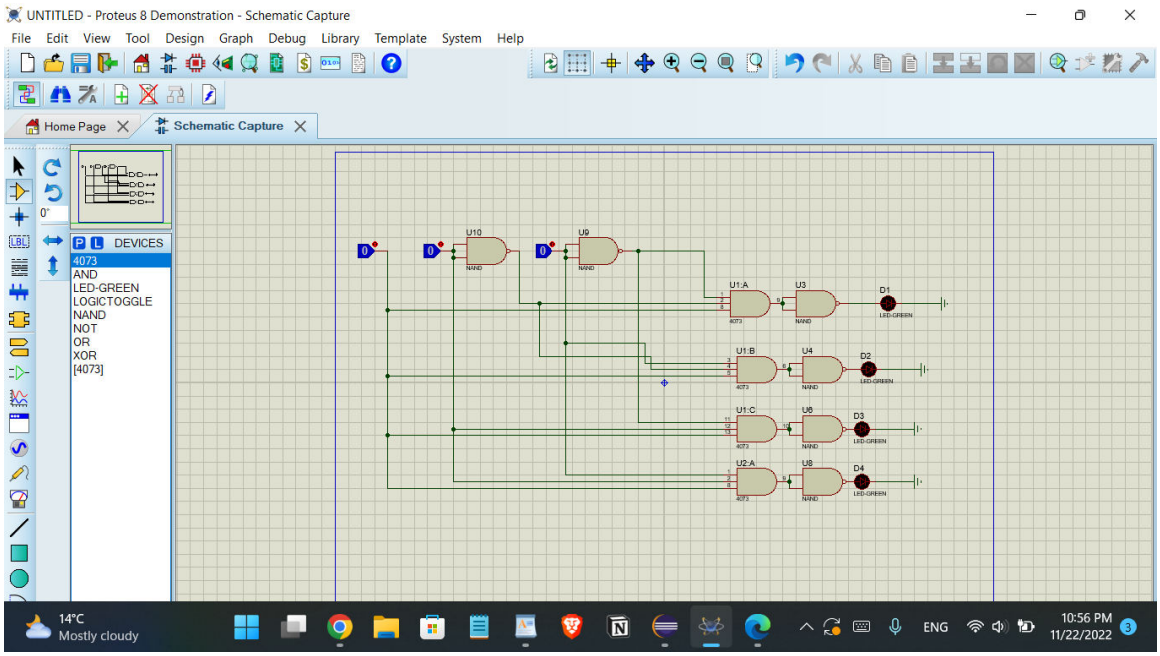
c) Build a 3x8 Decoder (active low) using basic gates.



d) Build an 8x1 Multiplexer using basic gates.

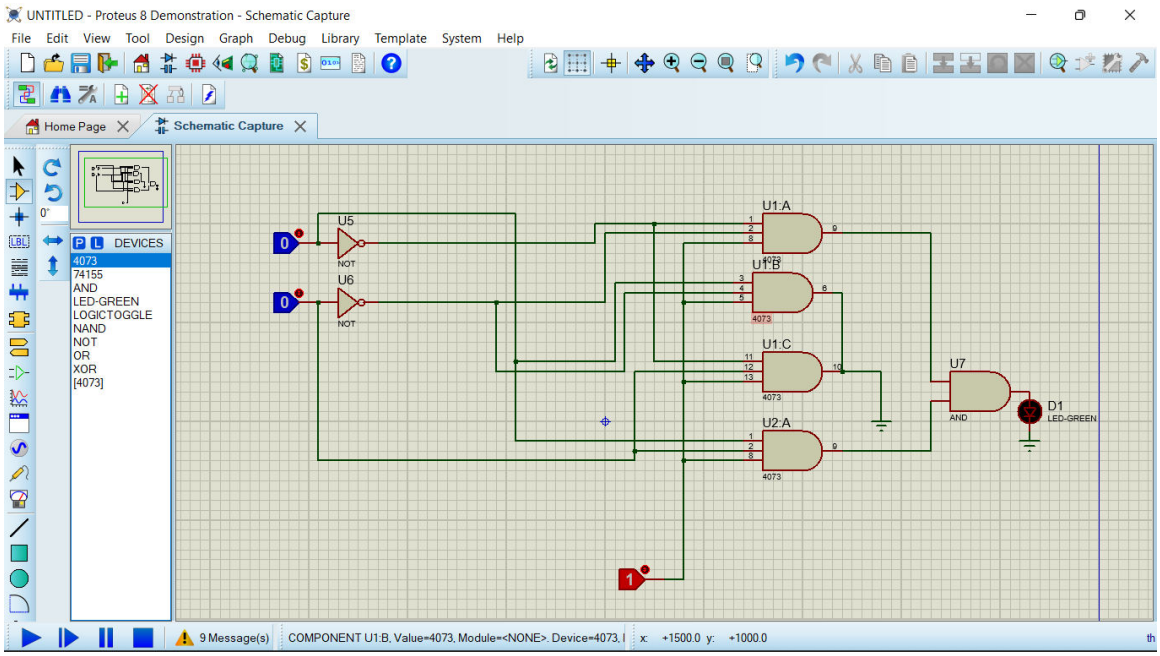


e) Build a 1x4 Demultiplexer using NAND gates



f)Use the 2x4 decoder to implement a 2 inputs function that acts like an equivalence gate

(XNOR): gives 1 on the output if both inputs are equal.


UNTITLED - Proteus 8 Demonstration - Schematic Capture
File Edit View Tool Design Graph Debug Library Template System Help
Home Page
Schematic Capture
DEVICES
4073
74155
AND
LED-GREEN
LOGICTOGGLE
NAND
NOT
OR
XOR
[4073]
U5
U6
NOT
U1:A
U1:B
U1:C
U2:A
4073
U7
AND
D1
LED-GREEN
9 Message(s)
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